

TORINO 2

PCB Thickness:1mm

CPU :INTEL MEROM
Chip Set :INTEL 965GM & ICH8-M
Remarks :w/o INTEL AMT
2 SODIMMs

Model Name : TORINO 2
PBA Name : MAIN
PCB Code : BA41-00727A / 728A
Dev. Step : MP
Revision : 1.0
T.R. Date : 2007.03.09

DRAW	CHECK	APPROVAL
-	-	-

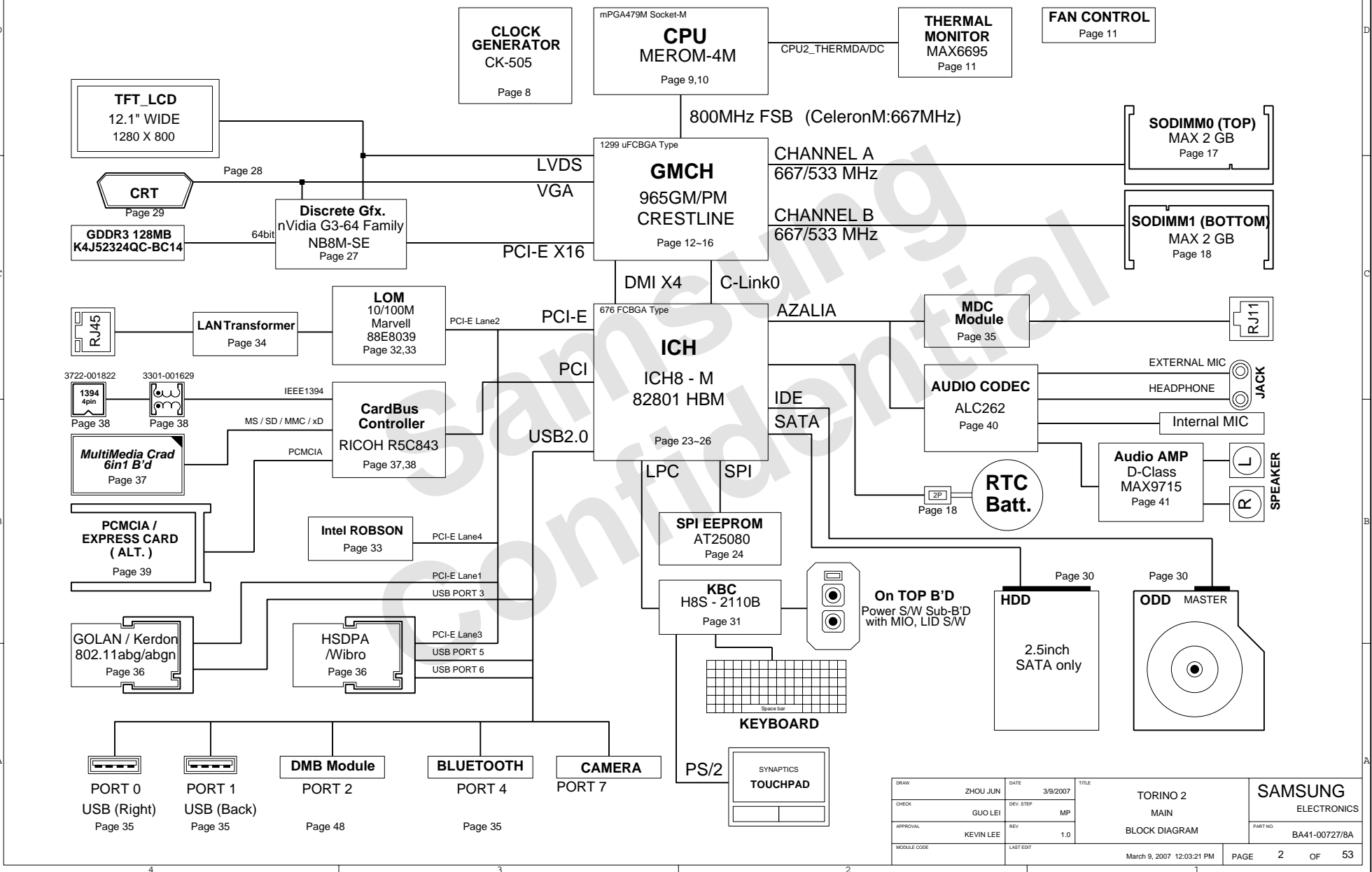
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DIAGRAM

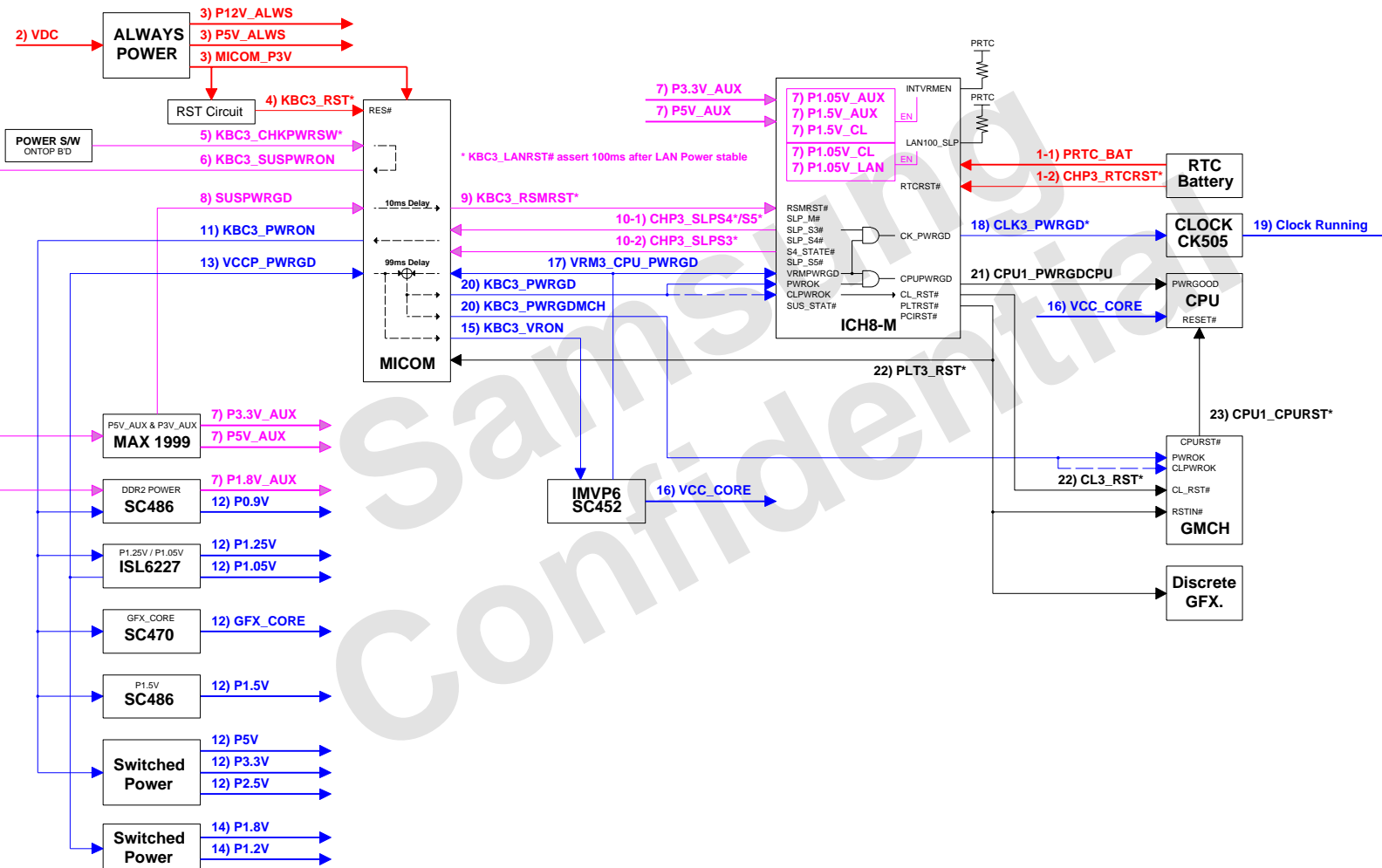
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APPROVAL	KEVIN LEE	REV	1.0		CONTENTS	PART NO.
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM			BA41-00727/8A
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OPERATION BLOCK DIAGRAM

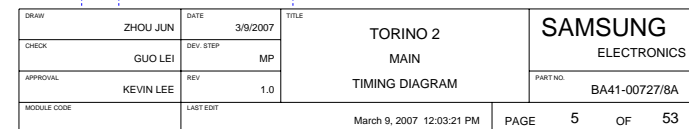


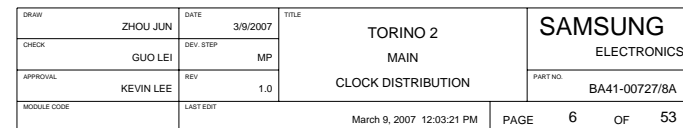
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POWER SEQUENCE Rev. 0.1



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BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	0	E,F,G

Voltage Rails

VDC	Primary DC system power supply (7 to 21V)
CPU_CORE	Core voltage for Processor (1.308~1.068V)
P1.05V	Processor System Bus(PSB) Termination (1.05V)
GFX_CORE	GMCH & ICH8 Core Voltage
P1.8V_AUX	Core voltage for NB8M-SE (1.0 ~ 1.1V)
P0.9V	1.8V power rail for DDR2 (off in S4-S5)
P1.8V	0.9V switched power rail (off in S3-S5)
P1.2V	1.8V power rail for GDDR3 (off in S3-S5)
P1.5V	1.2V switched power rail (off in S3-S5)
P2.5V	1.5V switched power rail (off in S3-S5)
P3.3V	2.5V switched power rail (off in S3-S5)
P5.0V	3.3V switched power rail (off in S3-S5)
P3.3V_AUX	5.0V switched power rail (off in S3-S5)
P5V_AUX	3.3V power rail (off in S4-S5)
PRTC_BAT	5.0V power rail (off in S4-S5)
P3.3V_MICOM	3.0V power rail (ALWAYS ON)
P5.0V_ALW	3.3V always on power rail for MICOM
P12.0V_ALW	5V power rail (Always On)
	12V power rail (Always On)

I²C / SMB Address

Devices	Address	Hex	Bus
ICH8	Master	-	SMBUS Master
SODIMM0	1010 000X	A0h	-
SODIMM1	1010 010X	A4h	-
CK-505 (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable
MICOM	Master	-	SMBUS Master
BATTERY	0001 011X	16h	-
EMC2102(Termal Sensor)	0111 101X	7Ah	Thermal Sensor

USB PORT Assign

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1	SYSTEM PORT B
2	DMB CARD
3	MINI PCLE
4	BLUETOOTH
5	WIBRO SIM CARD
6	HSDPA
7	CAMERA

System Power States

CHP3_SLPS1* S1, Powered-On-Suspend(POS) : In this state, all clocks(except the 32.768KHz clock) are stopped.
The system context is maintained in system DRAM. Power is maintained to PCI, the CPU, memory controller, memory, and all other critical subsystems.
Note that this state does not preclude power being removed from non-essential devices, such as disk drives. During this state, CPU can be selected for either Deep Sleep or Deeper Sleep.
CHP3_SLPS3* S3, Suspend-To-RAM(STR) : The system context is maintained in system DRAM, but power is shut off to non-critical circuits.
Memory is retained, and refreshes continue. All clocks stop except RTC clock.
CHP3_SLPS4* S4, Suspend-To-Disk(STD) : The Context of the system is maintained on the disk. All power is then shut off to the system except for the logic required to resume.
Externally appears same as S5, but may have different wake events.
CHP3_SLPS5* S5, Soft Off(SOFF) : System context is not maintained. All power is shut off except for the logic required to restart. A full boot is required when waking.

Crystal / Oscillator

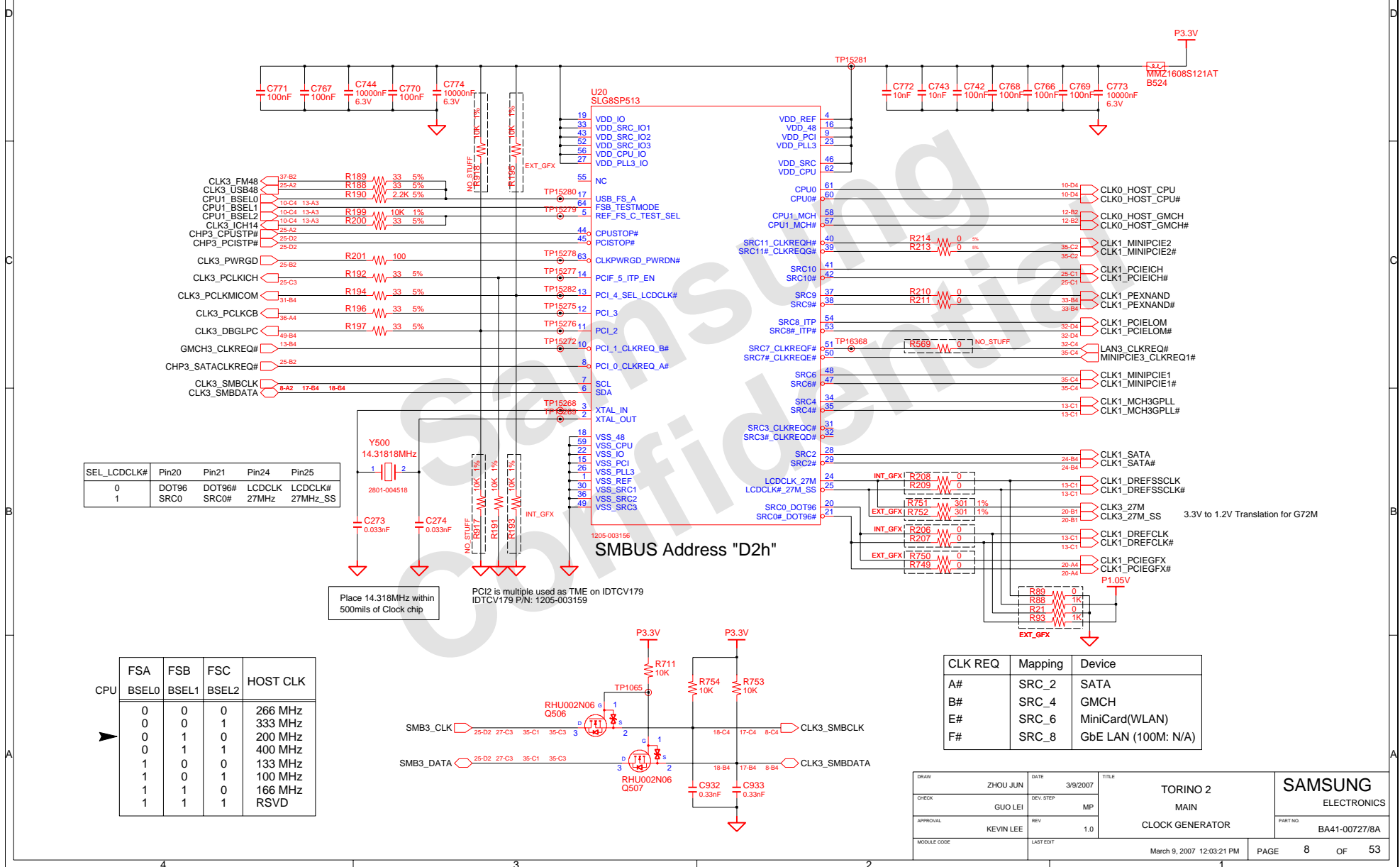
TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	ICH8-M	Real Time Clock
Crystal	10MHz	MICOM	H8S/2110BV
Crystal	14.318MHz	CLOCK-Generator	CK-505
Crystal	24.576MHz	Cardbus Controller	1394
Crystal	25MHz	LAN	LAN

CPU Core Voltage Table

IMVP-6

Active Mode		Active/Deeper Sleep Dual Mode Region		Deeper Sleep/Extended Deeper Sleep Dual Mode Region	
VID(6:0)	Voltage	VID(6:0)	Voltage	VID(6:0)	Voltage
0 0 0 0 0 0 0	1.5000 V	0 1 0 1 0 0 0	1.0000 V	1 0 1 0 0 0 1	0.4875 V
0 0 0 0 0 0 1	1.4875 V	0 1 0 1 0 0 1	0.9875 V	1 0 1 0 0 1 0	0.4750 V
0 0 0 0 0 1 0	1.4750 V	0 1 0 1 0 1 0	0.9750 V	1 0 1 0 0 1 1	0.4625 V
0 0 0 0 0 1 1	1.4625 V	0 1 0 1 1 0 1	0.9625 V	1 0 1 0 1 0 0	0.4500 V
0 0 0 0 1 0 0	1.4500 V	0 1 0 1 1 0 0	0.9500 V	1 0 1 0 1 0 1	0.4375 V
0 0 0 0 1 0 1	1.4375 V	0 1 0 1 1 1 0	0.9375 V	1 0 1 0 1 1 0	0.4250 V
0 0 0 0 1 1 0	1.4250 V	0 1 0 1 1 1 1	0.9250 V	1 0 1 0 1 1 1	0.4125 V
0 0 0 0 1 1 1	1.4125 V	0 1 0 1 1 1 1	0.9125 V	1 0 1 1 1 0 0	0.4000 V
0 0 0 1 0 0 0	1.4000 V	0 1 1 0 0 0 0	0.9000 V	1 0 1 1 1 0 1	0.3875 V
0 0 0 1 0 0 1	1.3875 V	0 1 1 0 0 0 1	0.8875 V	1 0 1 1 1 0 1	0.3750 V
0 0 0 1 0 1 0	1.3750 V	0 1 1 0 0 1 0	0.8750 V	1 0 1 1 1 0 1	0.3625 V
0 0 0 1 0 1 1	1.3625 V	0 1 1 0 1 0 1	0.8625 V	1 0 1 1 1 1 0	0.3500 V
0 0 0 1 1 0 0	1.3500 V	0 1 1 0 1 0 0	0.8500 V	1 0 1 1 1 1 0	0.3375 V
0 0 0 1 1 0 1	1.3375 V	0 1 1 0 1 1 0	0.8375 V	1 0 1 1 1 1 1	0.3250 V
0 0 0 1 1 1 0	1.3250 V	0 1 1 1 0 1 0	0.8250 V	1 0 1 1 1 1 1	0.3125 V
0 0 0 1 1 1 1	1.3125 V	0 1 1 1 0 1 1	0.8125 V	1 1 0 0 0 0 0	0.3000 V
0 0 1 0 0 0 0	1.3000 V	0 1 1 1 0 1 1	0.8000 V	1 1 0 0 0 0 1	0.2875 V
0 0 1 0 0 0 1	1.2875 V	0 1 1 1 0 1 0	0.7875 V	1 1 0 0 0 1 0	0.2750 V
0 0 1 0 0 1 0	1.2750 V	0 1 1 1 0 1 1	0.7750 V	1 1 0 0 0 1 1	0.2625 V
0 0 1 0 0 1 1	1.2625 V	0 1 1 1 1 0 1	0.7625 V	1 1 0 0 1 0 0	0.2500 V
0 0 1 0 1 0 0	1.2500 V	0 1 1 1 1 0 0	0.7500 V	1 1 0 0 1 0 1	0.2375 V
0 0 1 0 1 0 1	1.2375 V	0 1 1 1 1 1 0	0.7375 V	1 1 0 0 1 1 0	0.2250 V
0 0 1 0 1 1 0	1.2250 V	0 1 1 1 1 1 1	0.7250 V	1 1 0 0 1 1 1	0.2125 V
0 0 1 0 1 1 1	1.2125 V	0 1 1 1 1 1 1	0.7125 V	1 1 0 1 0 0 0	0.2000 V
0 0 1 1 0 0 0	1.2000 V	1 0 0 0 0 0 0	0.7000 V	1 1 0 1 0 0 1	0.1875 V
0 0 1 1 0 0 1	1.1875 V	1 0 0 0 0 0 1	0.6875 V	1 1 0 1 0 1 0	0.1750 V
0 0 1 1 0 1 0	1.1750 V	1 0 0 0 0 1 0	0.6750 V	1 1 0 1 0 1 1	0.1625 V
0 0 1 1 0 1 1	1.1625 V	1 0 0 0 1 0 1	0.6625 V	1 1 0 1 1 0 0	0.1500 V
0 0 1 1 1 0 0	1.1500 V	1 0 0 0 1 0 0	0.6500 V	1 1 0 1 1 0 1	0.1375 V
0 0 1 1 1 0 1	1.1375 V	1 0 0 0 1 1 0	0.6375 V	1 1 0 1 1 1 0	0.1250 V
0 0 1 1 1 1 0	1.1250 V	1 0 0 0 1 1 1	0.6250 V	1 1 0 1 1 1 1	0.1125 V
0 0 1 1 1 1 1	1.1125 V	1 0 0 0 1 1 1	0.6125 V	1 1 1 0 0 0 0	0.1000 V
0 1 0 0 0 0 0	1.1000 V	1 0 0 1 0 0 0	0.6000 V	1 1 1 0 0 0 1	0.0875 V
0 1 0 0 0 0 1	1.0875 V	1 0 0 1 0 0 1	0.5875 V	1 1 1 0 0 1 0	0.0750 V
0 1 0 0 0 1 0	1.0750 V	1 0 0 1 0 1 0	0.5750 V	1 1 1 0 0 1 1	0.0625 V
0 1 0 0 0 1 1	1.0625 V	1 0 0 1 0 1 1	0.5625 V	1 1 1 0 1 0 0	0.0500 V
0 1 0 0 1 0 0	1.0500 V	1 0 0 1 1 0 0	0.5500 V	1 1 1 0 1 0 1	0.0375 V
0 1 0 0 1 0 1	1.0375 V	1 0 0 1 1 0 1	0.5375 V	1 1 1 0 1 1 0	0.0250 V
0 1 0 0 1 1 0	1.0250 V	1 0 0 1 1 1 0	0.5250 V	1 1 1 0 1 1 1	0.0125 V
0 1 0 0 1 1 1	1.0125 V	1 0 0 1 1 1 1	0.5125 V	1 1 1 1 0 0 0	0.0000 V
		1 0 0 1 1 1 1	0.5000 V	1 1 1 1 0 0 1	0.0000 V
				1 1 1 1 0 1 0	0.0000 V
				1 1 1 1 0 1 1	0.0000 V
				1 1 1 1 1 0 0	0.0000 V
				1 1 1 1 1 0 1	0.0000 V
				1 1 1 1 1 1 0	0.0000 V
				1 1 1 1 1 1 1	0.0000 V
				**11111111* : 0V power good asserted.	

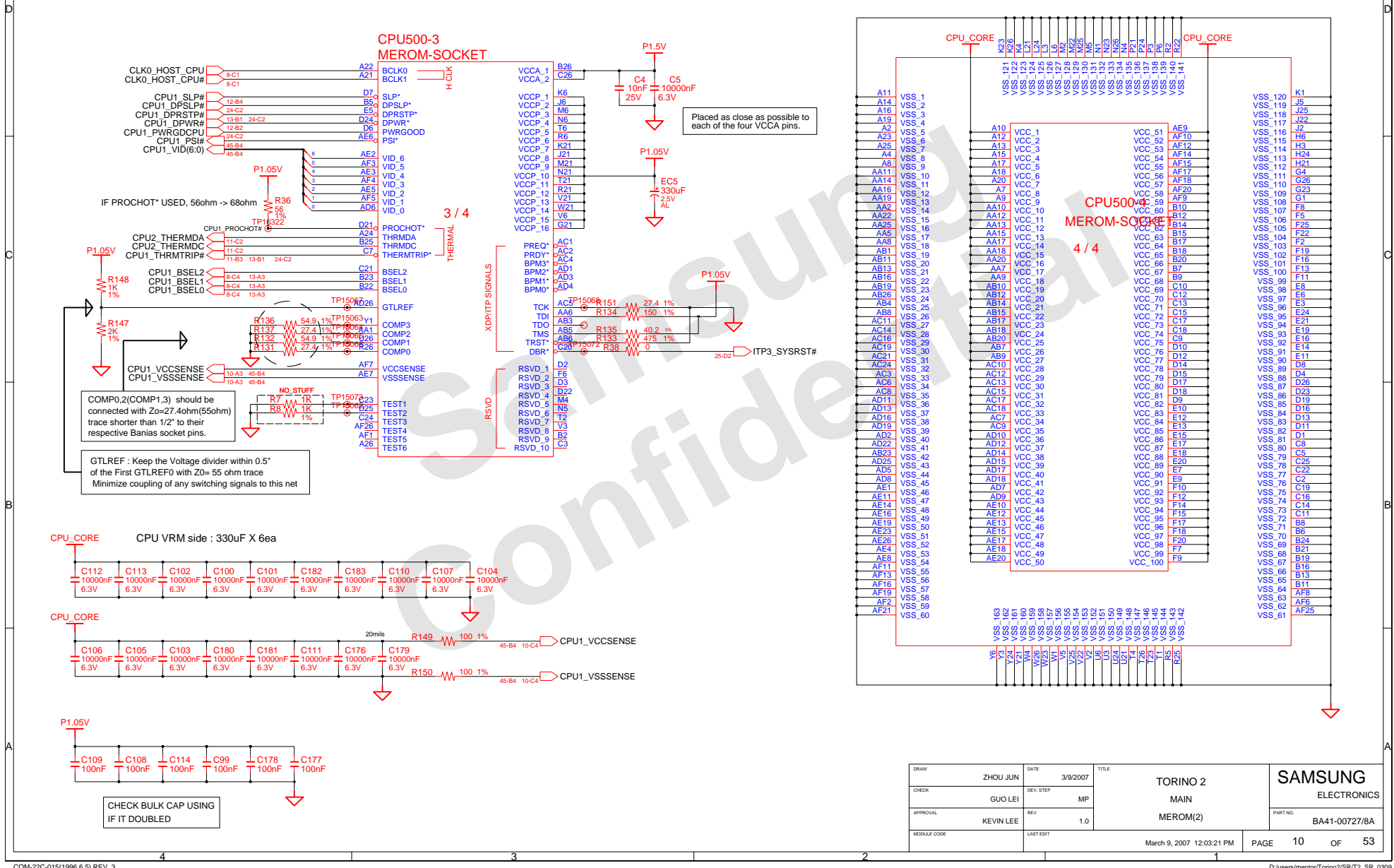
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APPROVAL	KEVIN LEE	REV	1.0		BOARD INFORMATION	PART NO.
MODULE CODE		LAST EDIT				BA41-00727/8A
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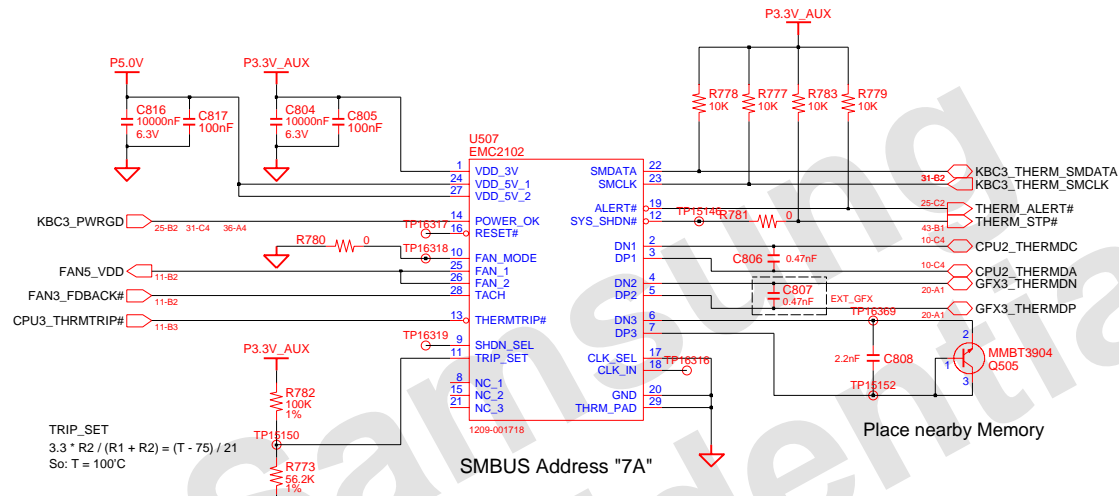
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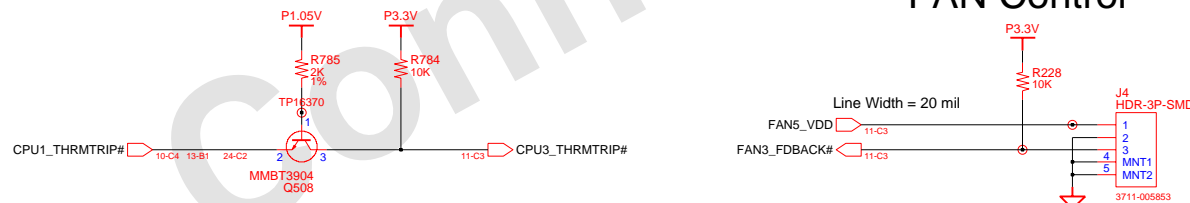
CPU SOCKET : MEROM



Thermal Monitor



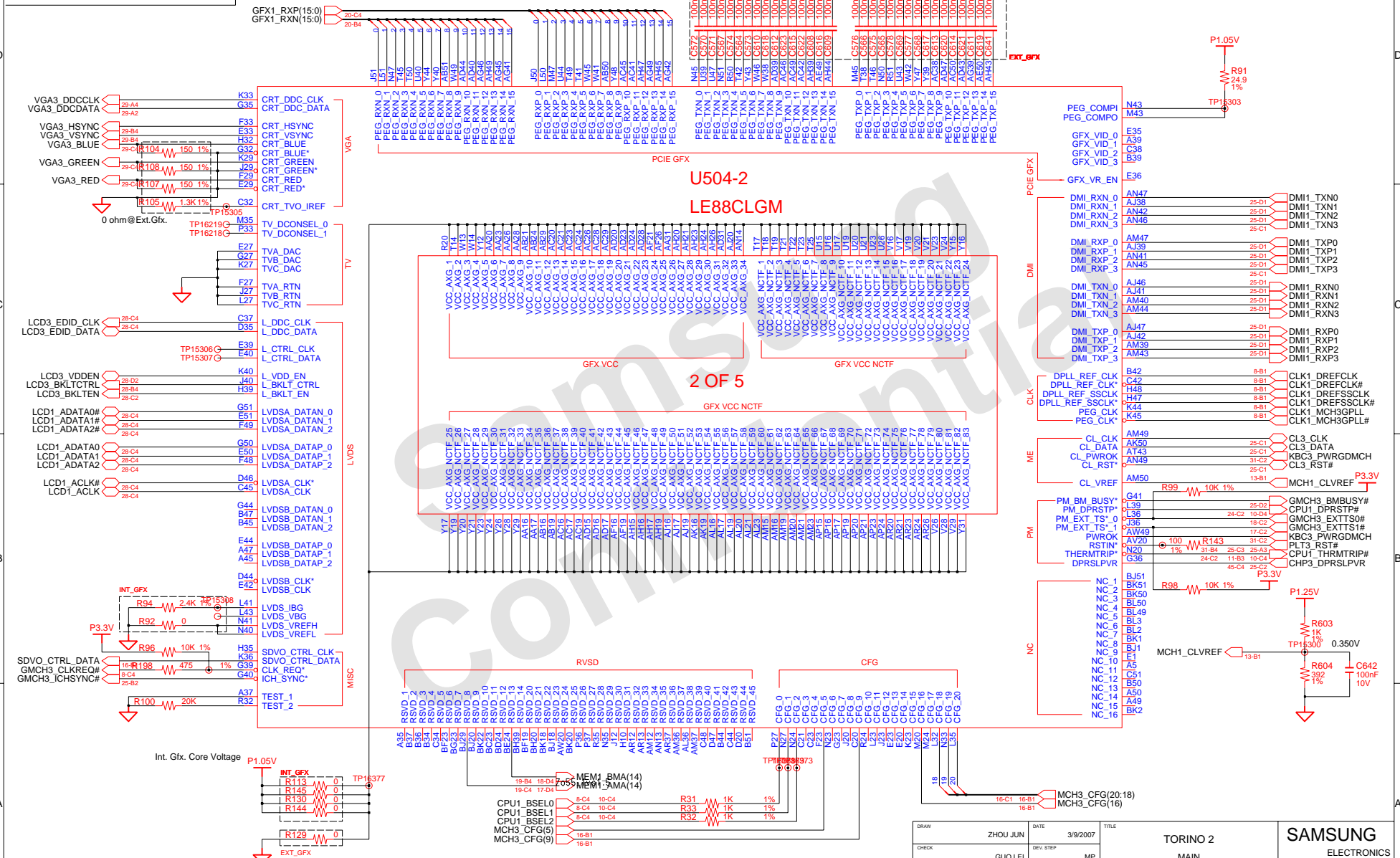
FAN Control



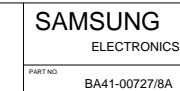
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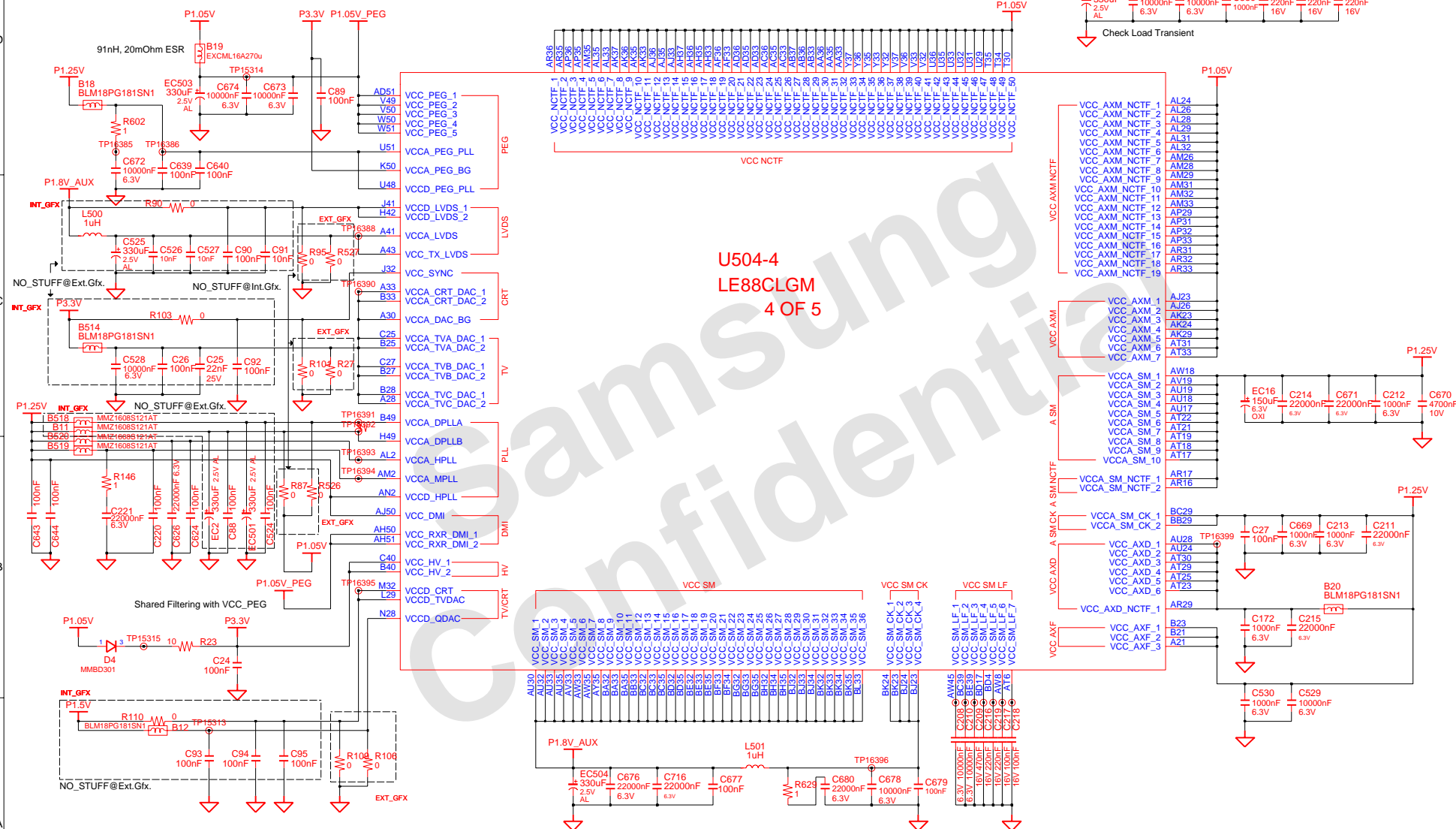
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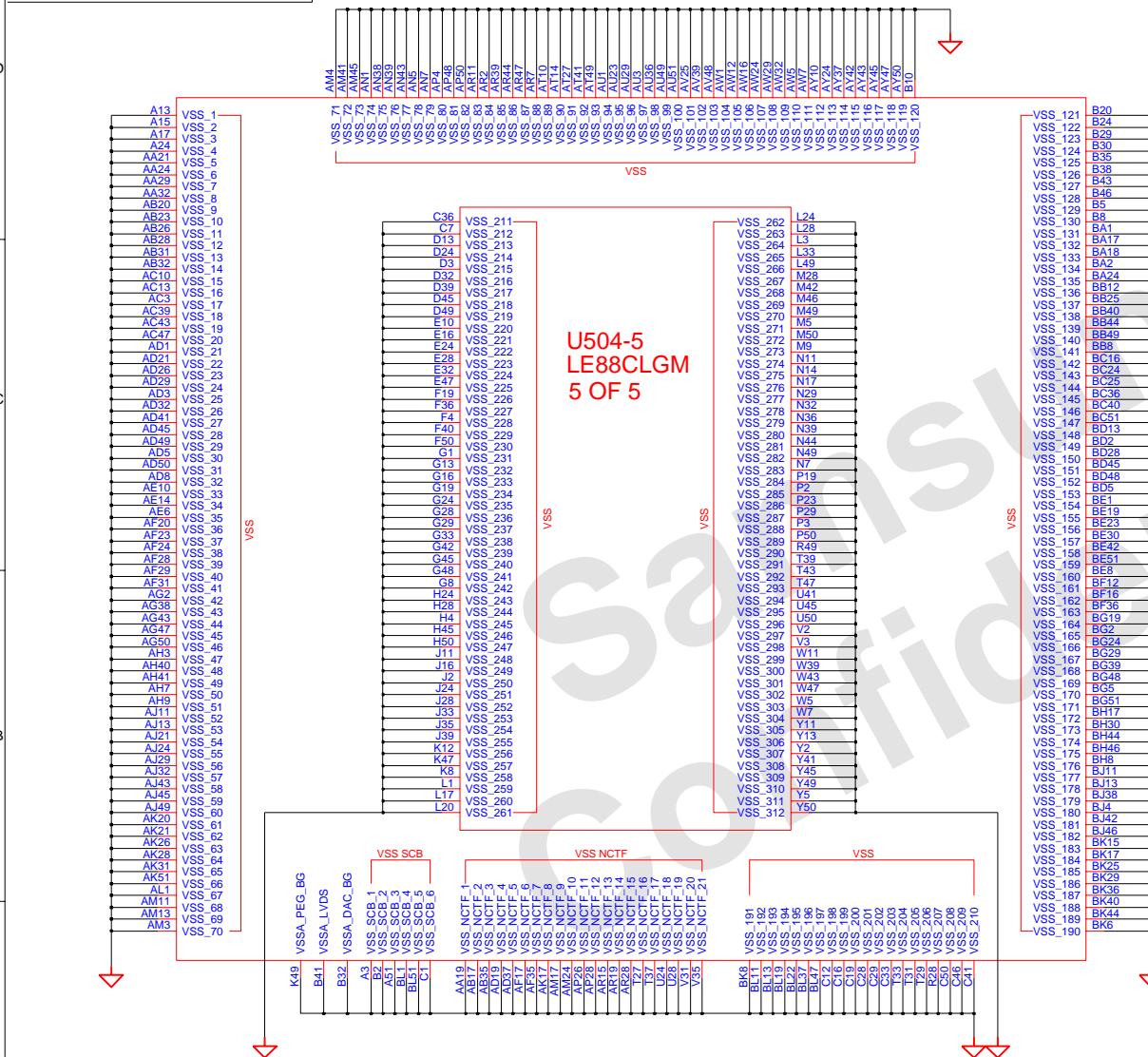


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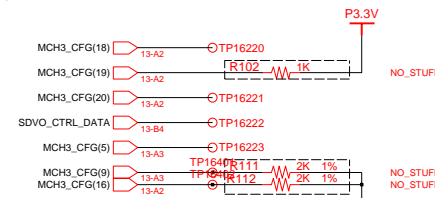
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** Note

CFG#	Current Setting (def. : default Option)	
	Low	High
CFG(5)	DMix2	DMix4 (def.)
CFG(9)	PEG Reversal	Normal
CFG(16)	Dynamic ODT	Dynamic ODT
	Disabled	Enabled (def.)
CFG(18)	VCC 1.05V (def.)	VCC 1.5V
CFG(19)	DMI Lane Normal	DMI Lane Reversal
CFG(20)	SDVO or PCIe X1	SDVO and PCIe X1
	Only (def.)	Simultaneously
SDVO_CTRL_DATA	No SDVO Device Present (def.)	SDVO Device Present

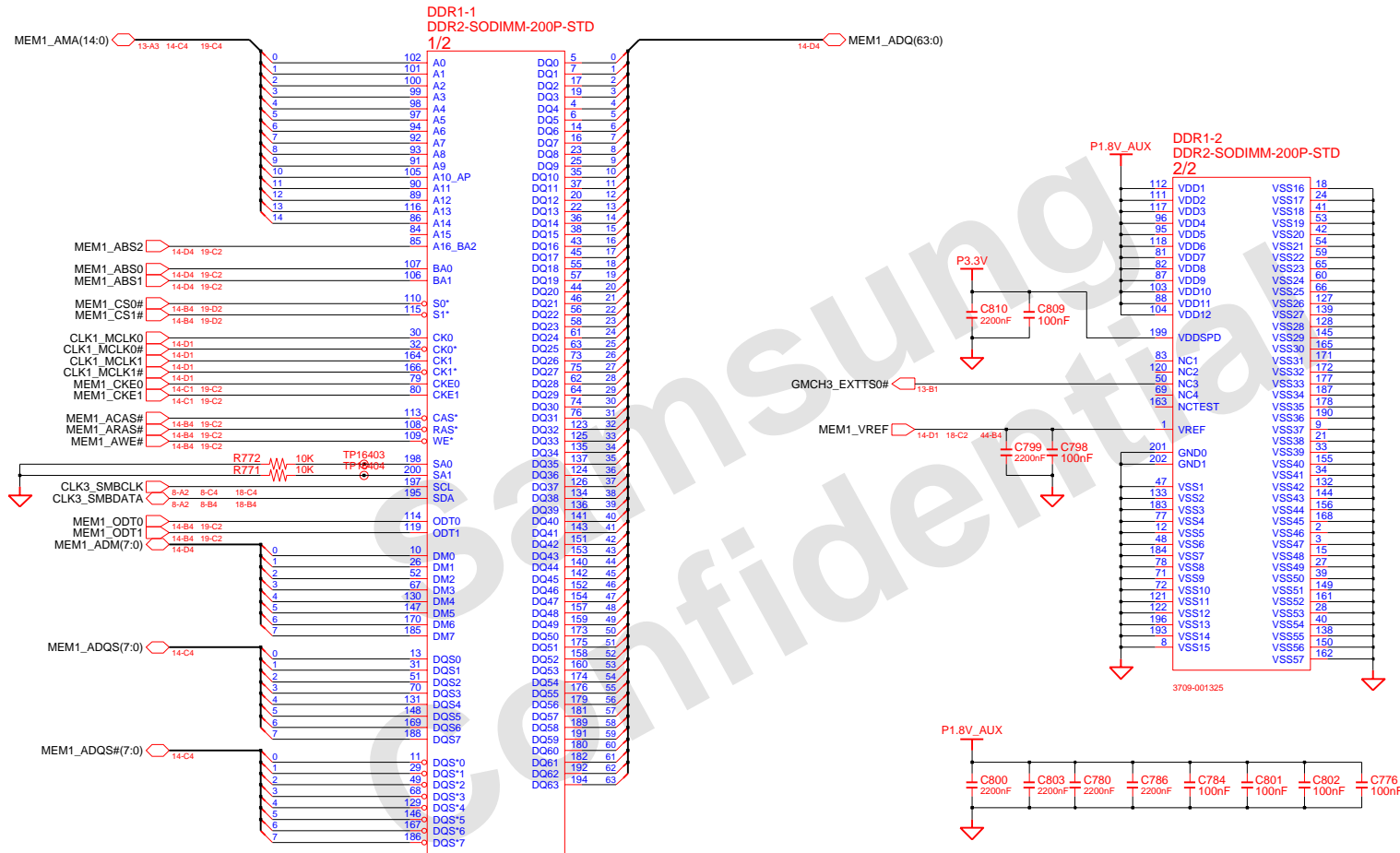


** Note
CFG(17:3) Internal Pull-up
CFG(20:18) Internal Pull-down

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CHECK	GUO LEI	DEV. STEP	MP		MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		965GM(5)	PART NO.
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SODIMM0 (TOP)



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CHECK	GUO LEI	DEV. STEP	MP	MAIN	
APPROVAL	KEVIN LEE	REV	1.0	DDR2 SODIMM (TOP)	
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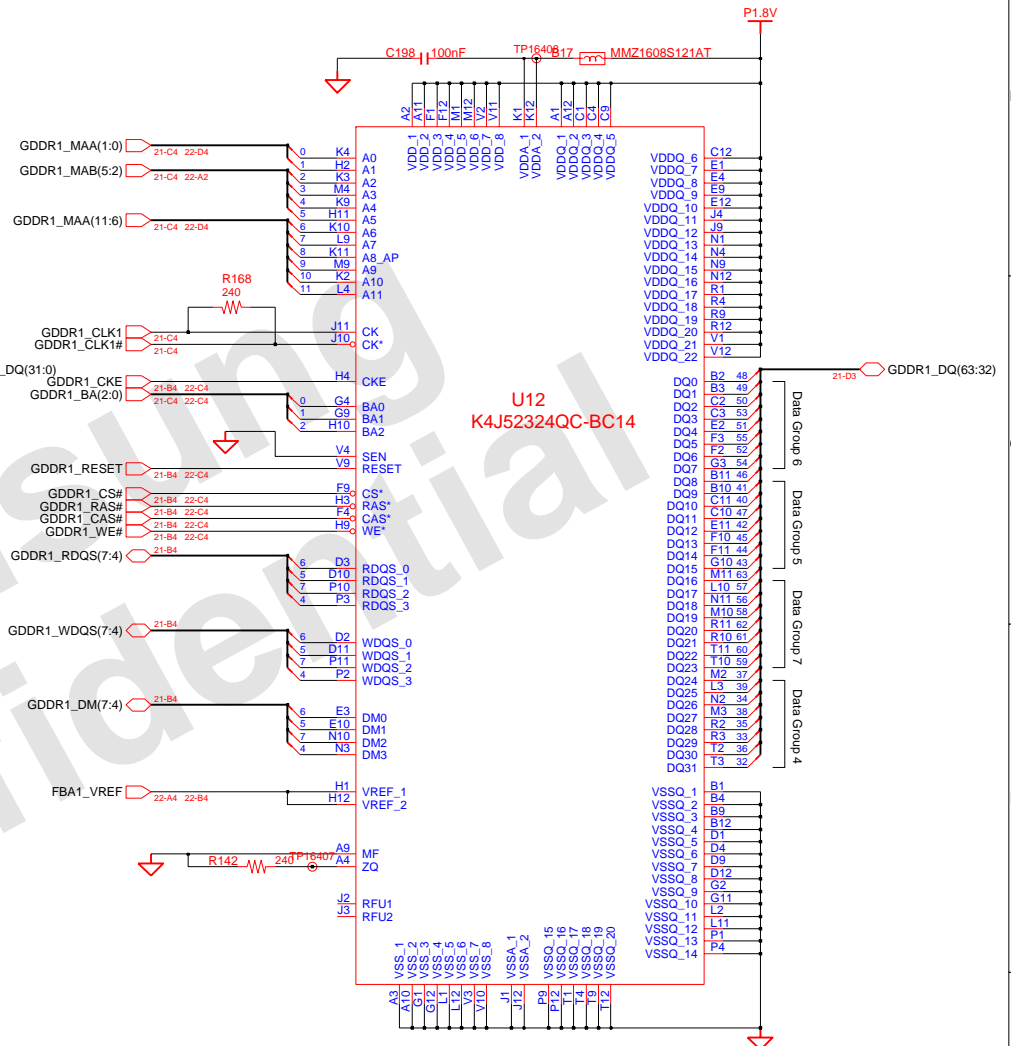
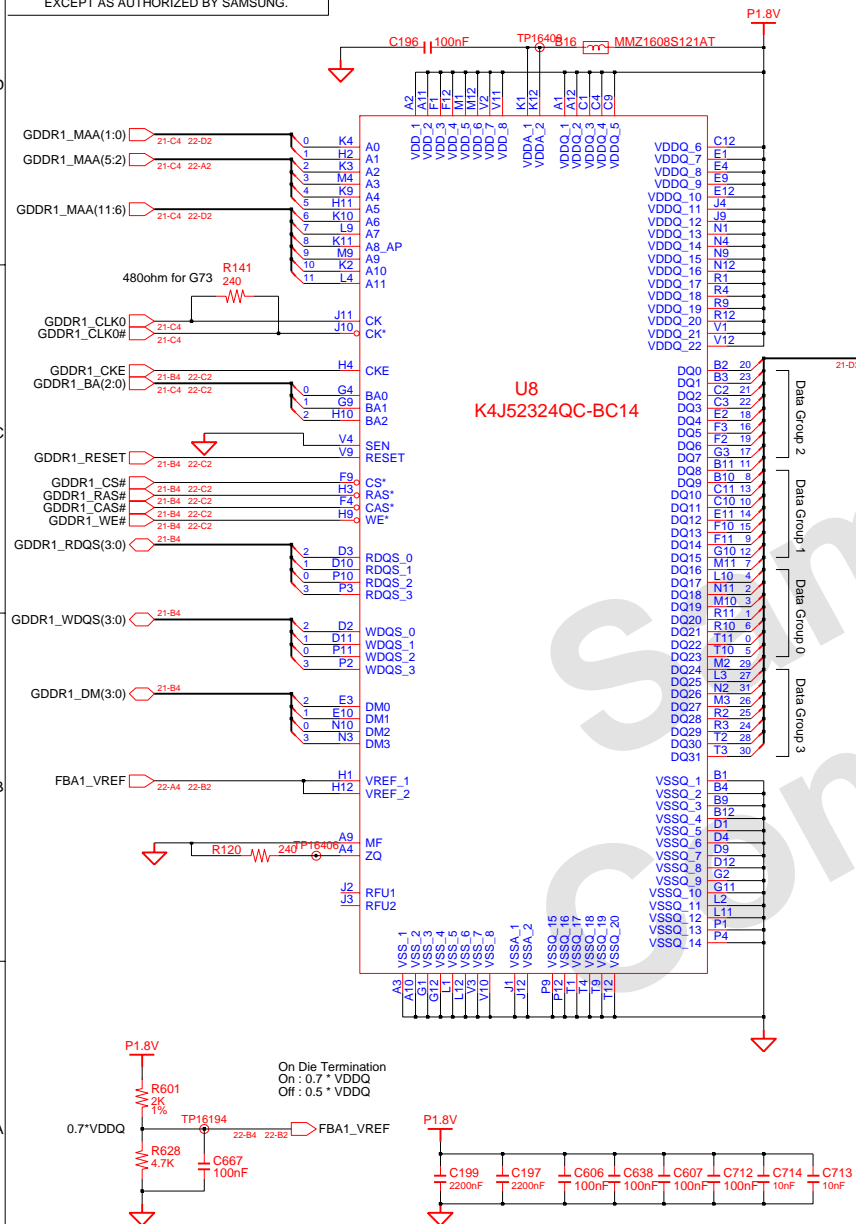
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CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	DDR2 TERMINATION	PART NO.	BA41-00727/8A
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EXT_GFX
All Instances on this Page should be NO_STUFF @ Int. Gfx.

D:/users/mentor/Torino2/SR/T2 SR_0309

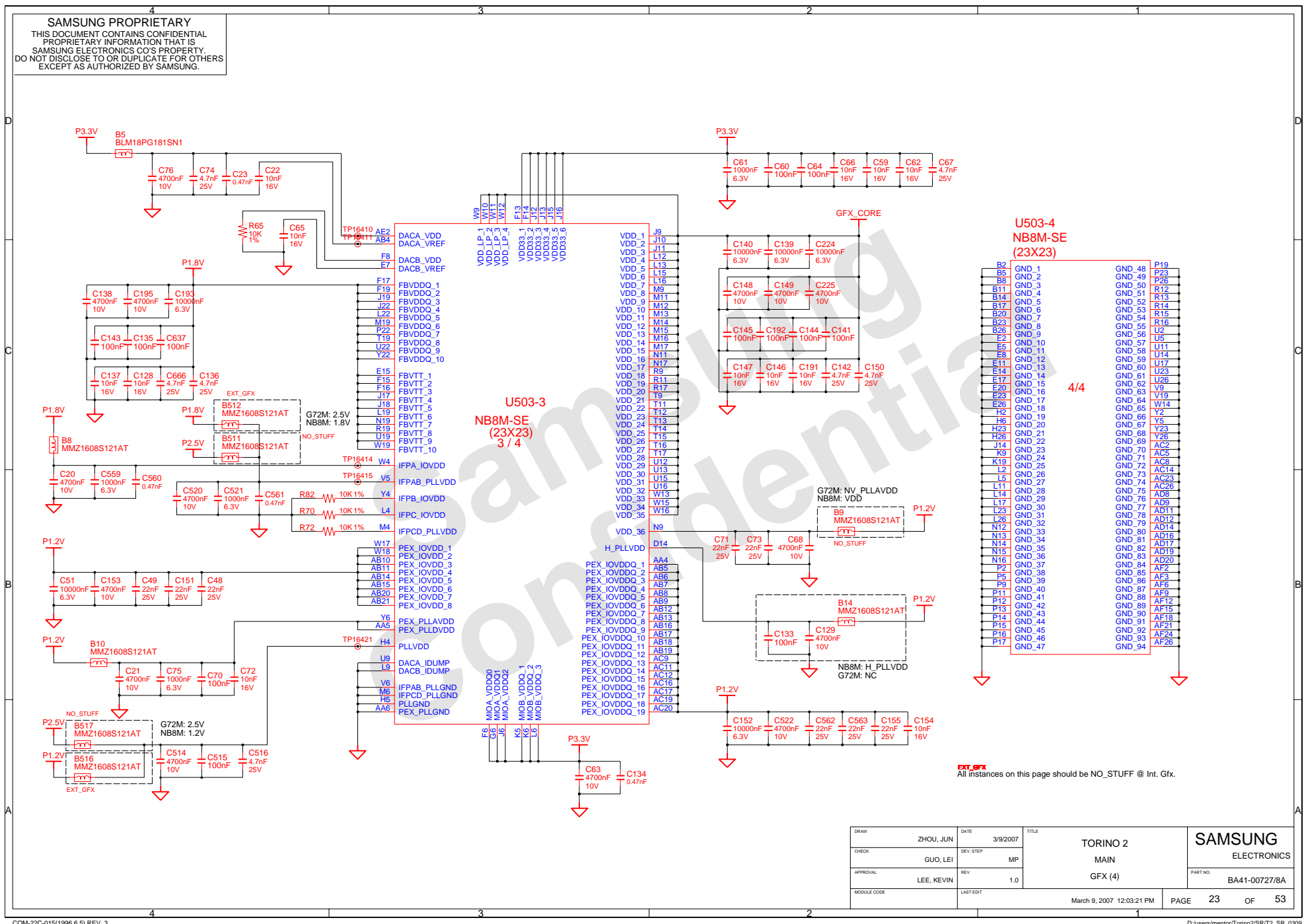
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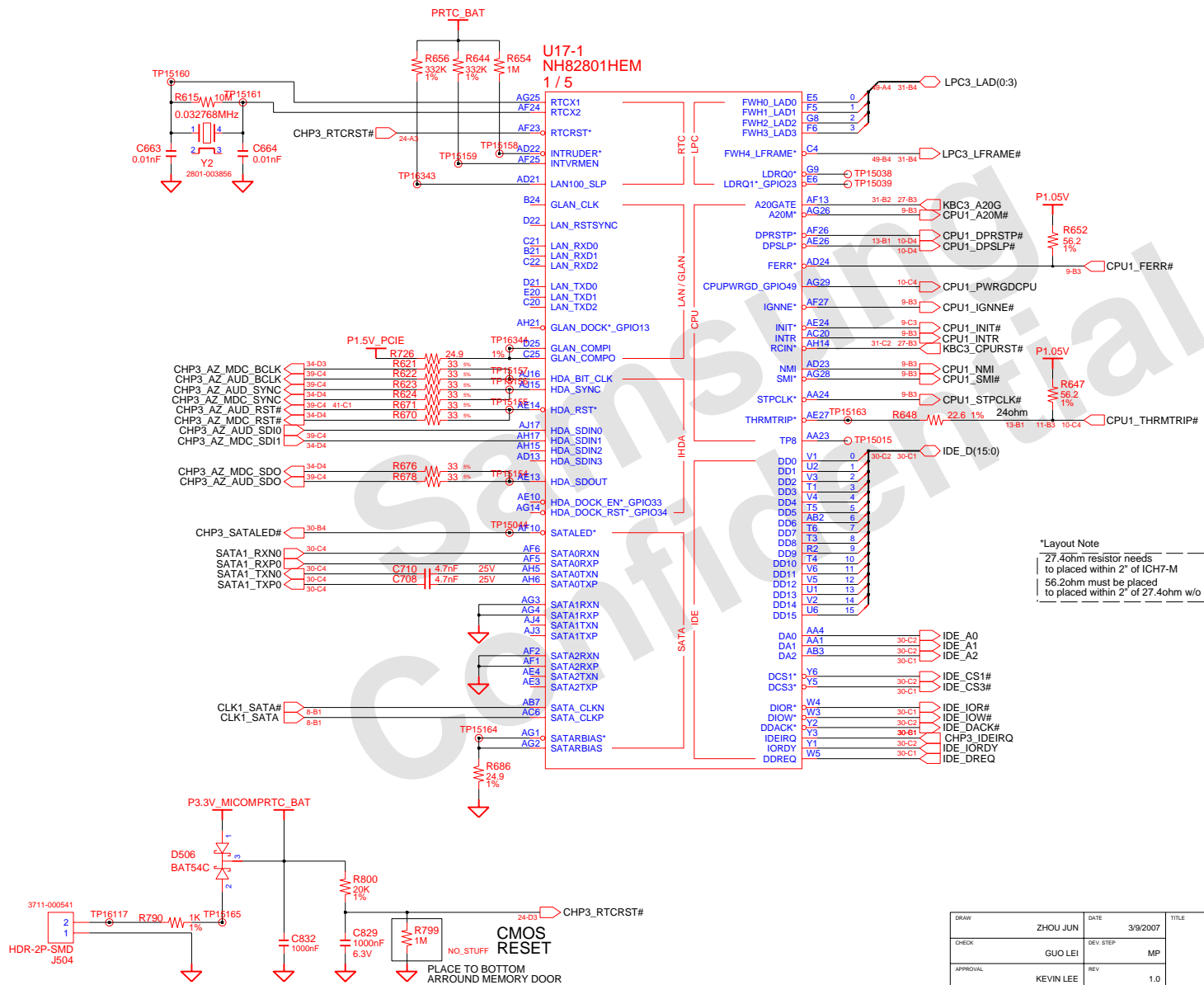
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APPROVAL	LEE, KEVIN	REV	1.0		GFX (3)	
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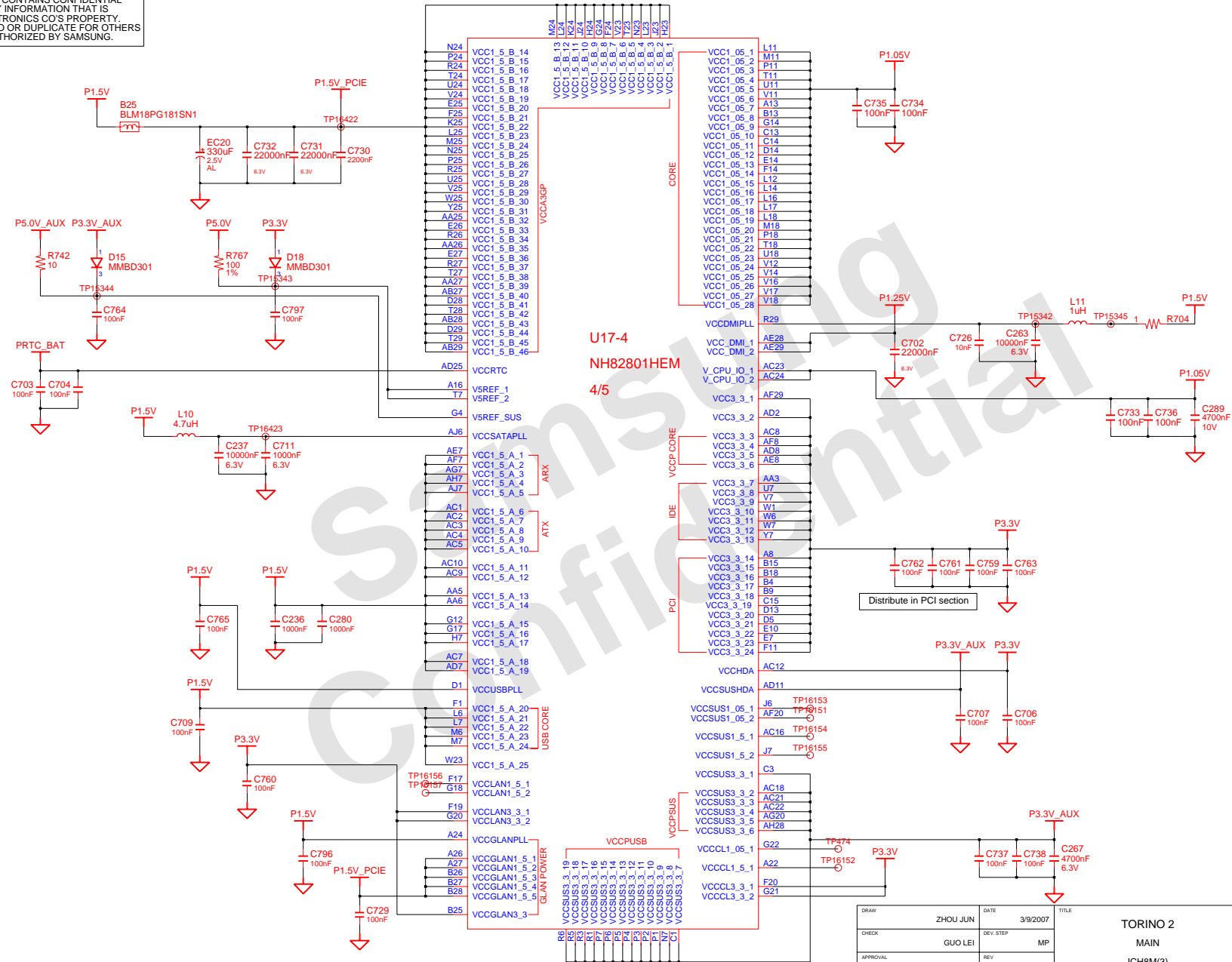
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*Layout Note
27.4ohm resistor needs
to be placed within 2" of ICH7-M
56.2ohm must be placed
to be placed within 2" of 27.4ohm w/o stub

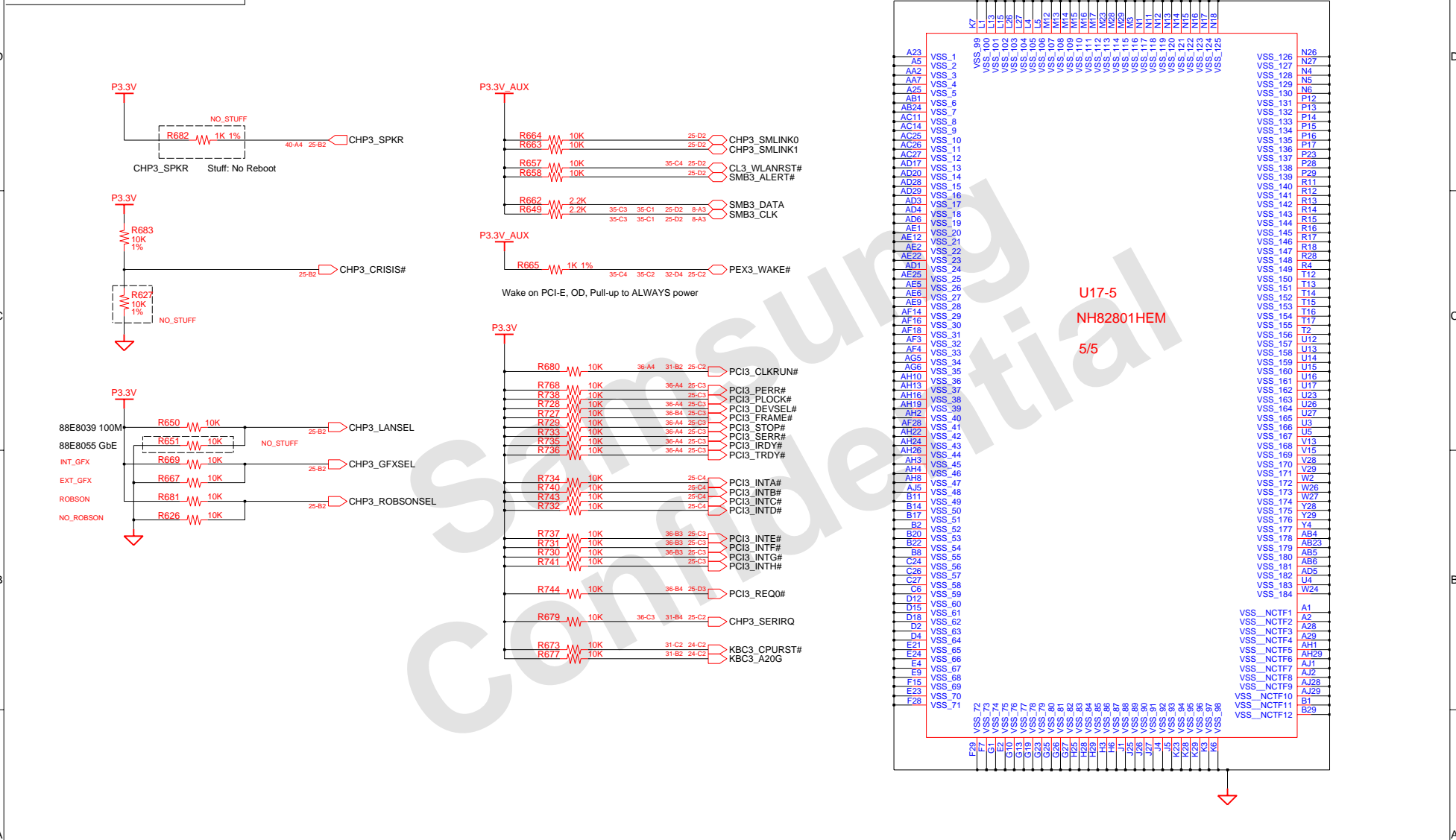
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MODULE CODE		LAST EDIT				BA41-00727/8A
				March 9, 2007 12:03:21 PM	PAGE	26 OF 53

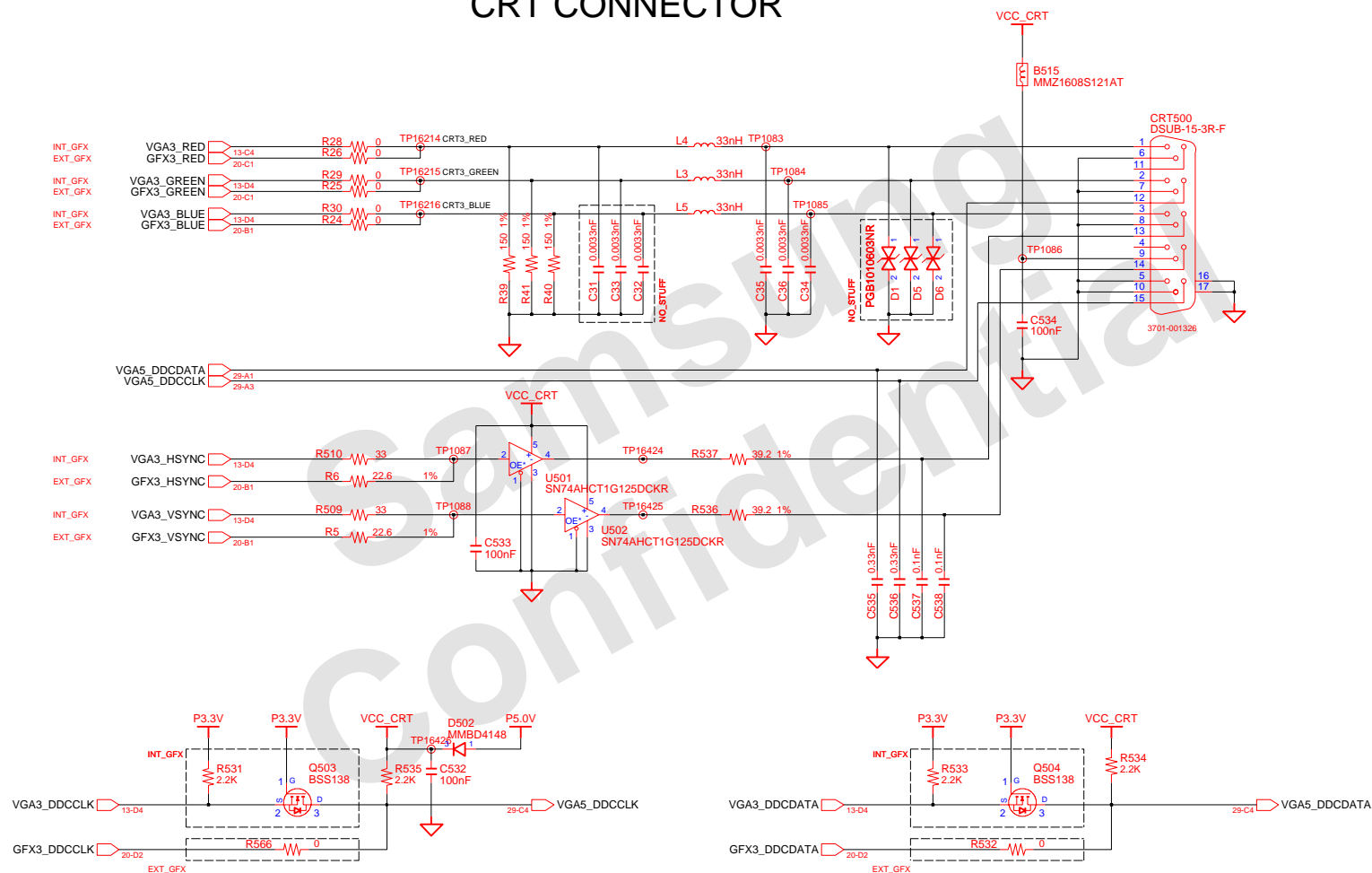
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DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	ICH8M(4)	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	27 OF 53

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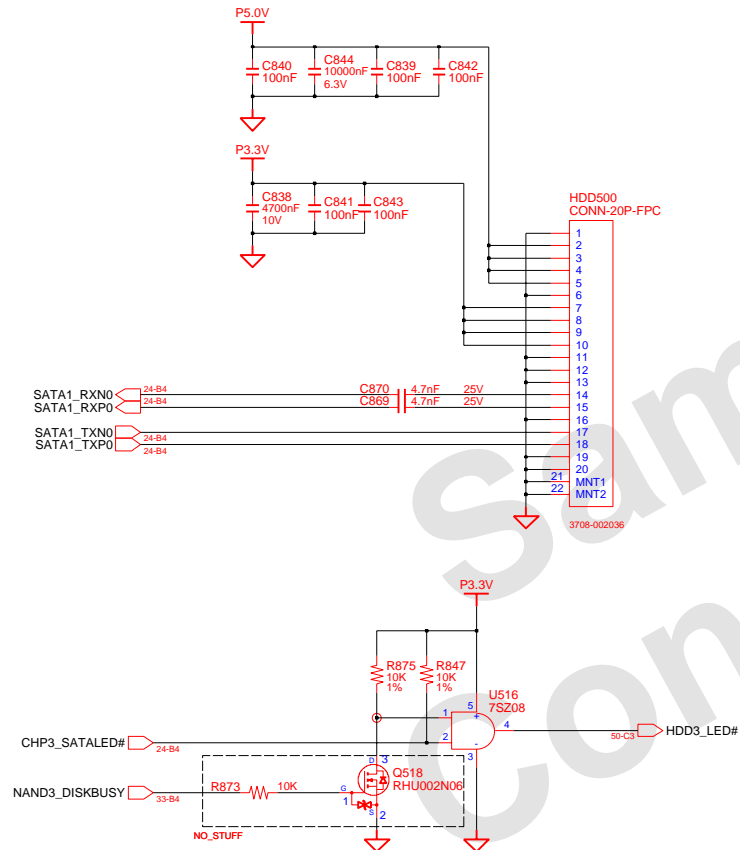
CRT CONNECTOR



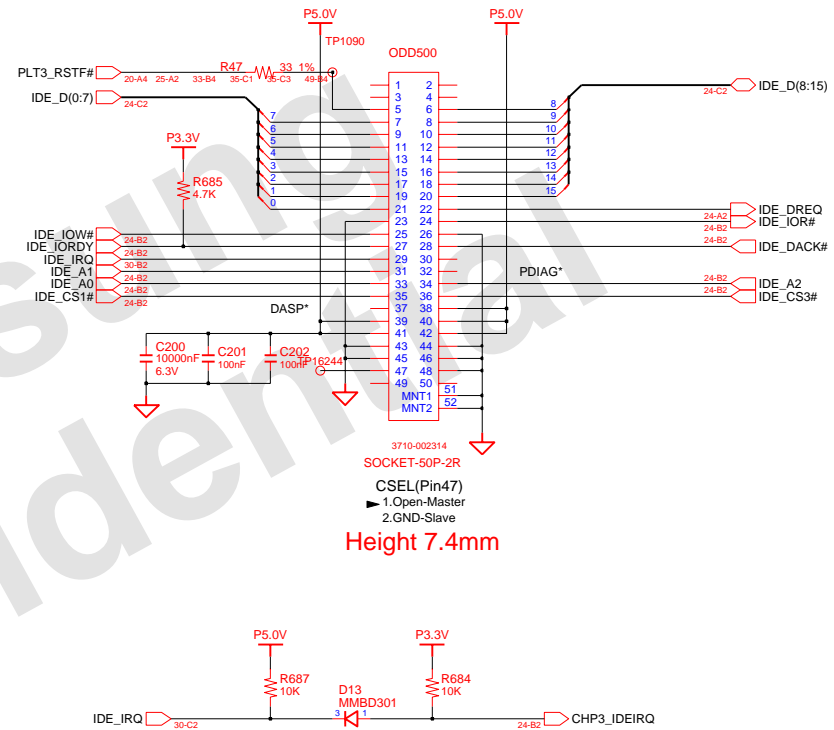
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CHECK	GUO LEI	DEV. STEP	MP					
APPROVAL	KEVIN LEE	REV	1.0					
MODULE CODE	LAST EDIT		March 9, 2007 12:03:21 PM				PAGE	29
						PART NO.	BA41-00727/8A	

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Main to HDD



Main to ODD



Height 7.4mm

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	ELECTRONICS	
APPROVAL	KEVIN LEE	REV	1.0	IDE	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	30	OF 53

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KBC5_BKLTON

KBC5_TCLK

KBC5_TDData

KBC3_EXTSMIF

KBC3_RUNSCI#

KBC3_SCI#

KBC3_NUMLED#

KBC3_CAPLED#

KBC3_LED_POWER#

KBC3_PWRGD

KBC3_PWRON

KBC5_KSI(0:7)

LPC3_LAD(0:3)

LPC3_LFRAME#

PLT3_RST#

CLK3_PCLKMICOM

CHP3_SERIRQ

P5.0V

P3.3V_MICOM

U15 H8S-2110B
0903-001439

MICOM RESET

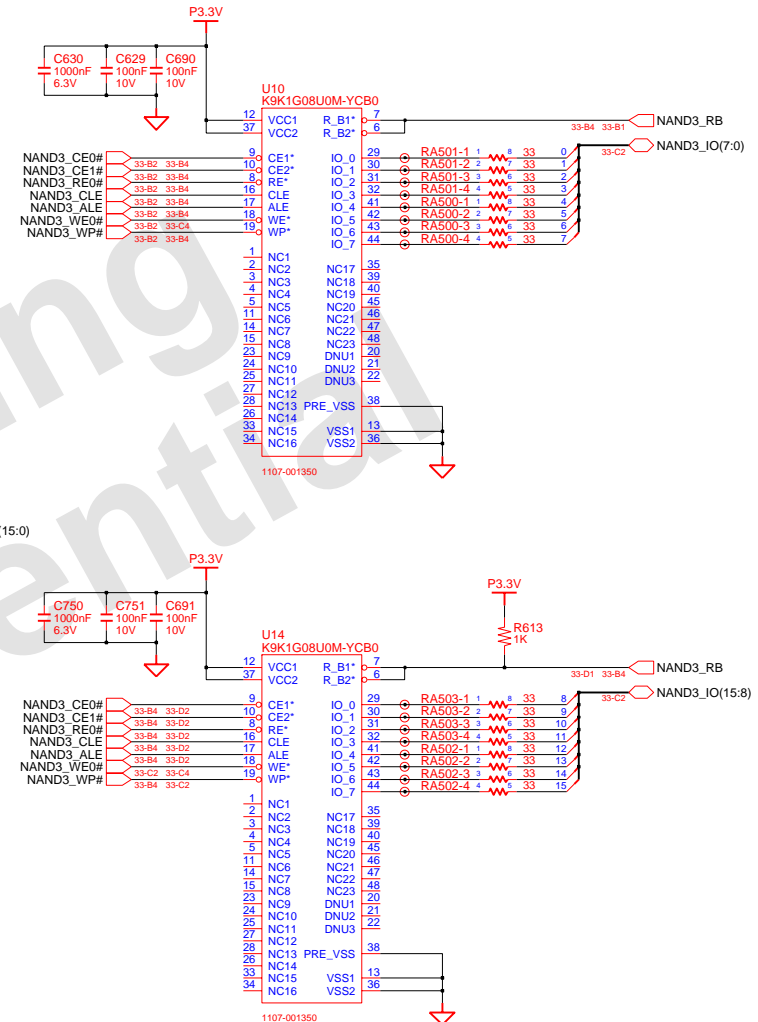
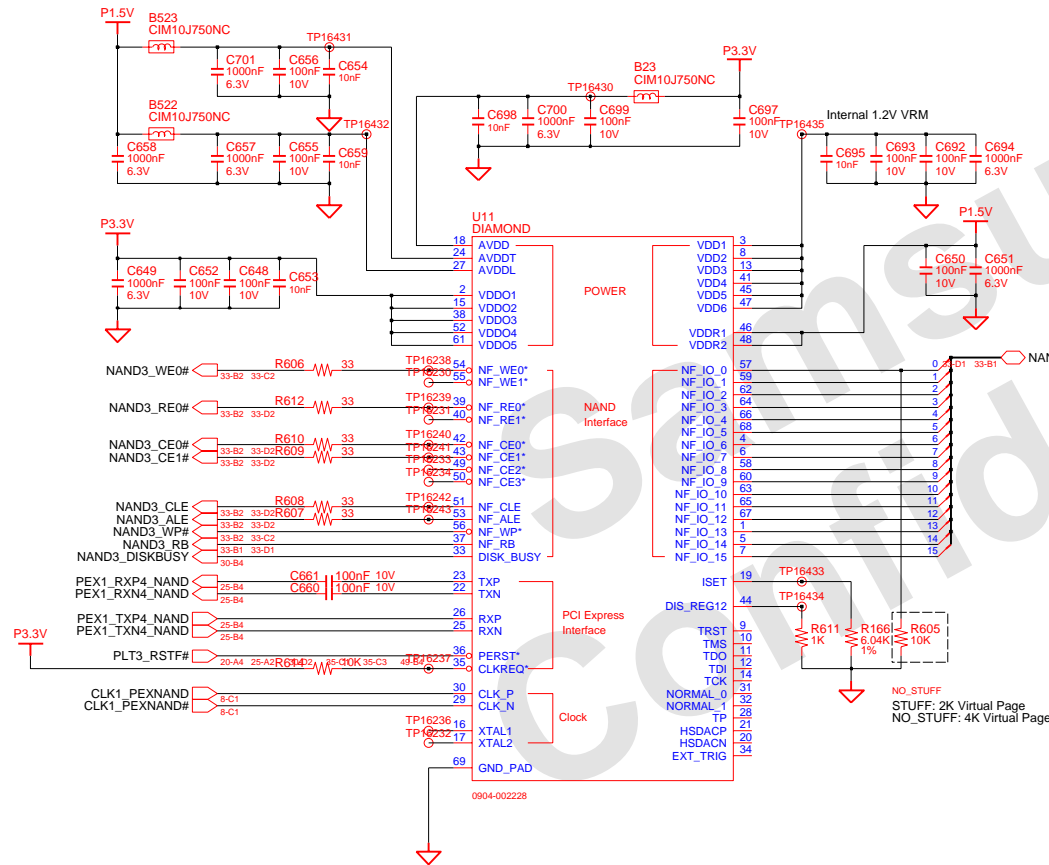
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ZHOU JUN	3/9/2007	TORINO 2	
CHECK	DEV. STEP	MAIN	
GUO LEI	MP	MICOM	
APPROVAL	REV	1.0	
KEVIN LEE			
MODULE CODE	LAST EDIT		

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[illegible]

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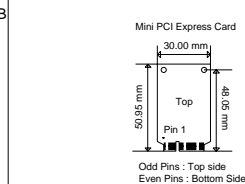
Robson



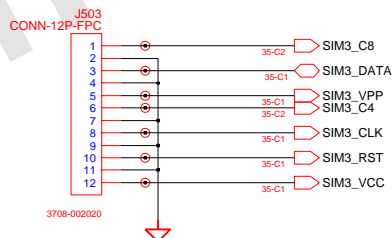
ROBSON
All instances on this page should be NO_STUFF if ROBSON is N/A

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		ROBSON	
MODULE CODE		LAST EDIT				
					March 9, 2007 12:03:21 PM	PAGE 33 OF 53

3



2



M2 HEAD
DIA
LENGTH
BA61-01090

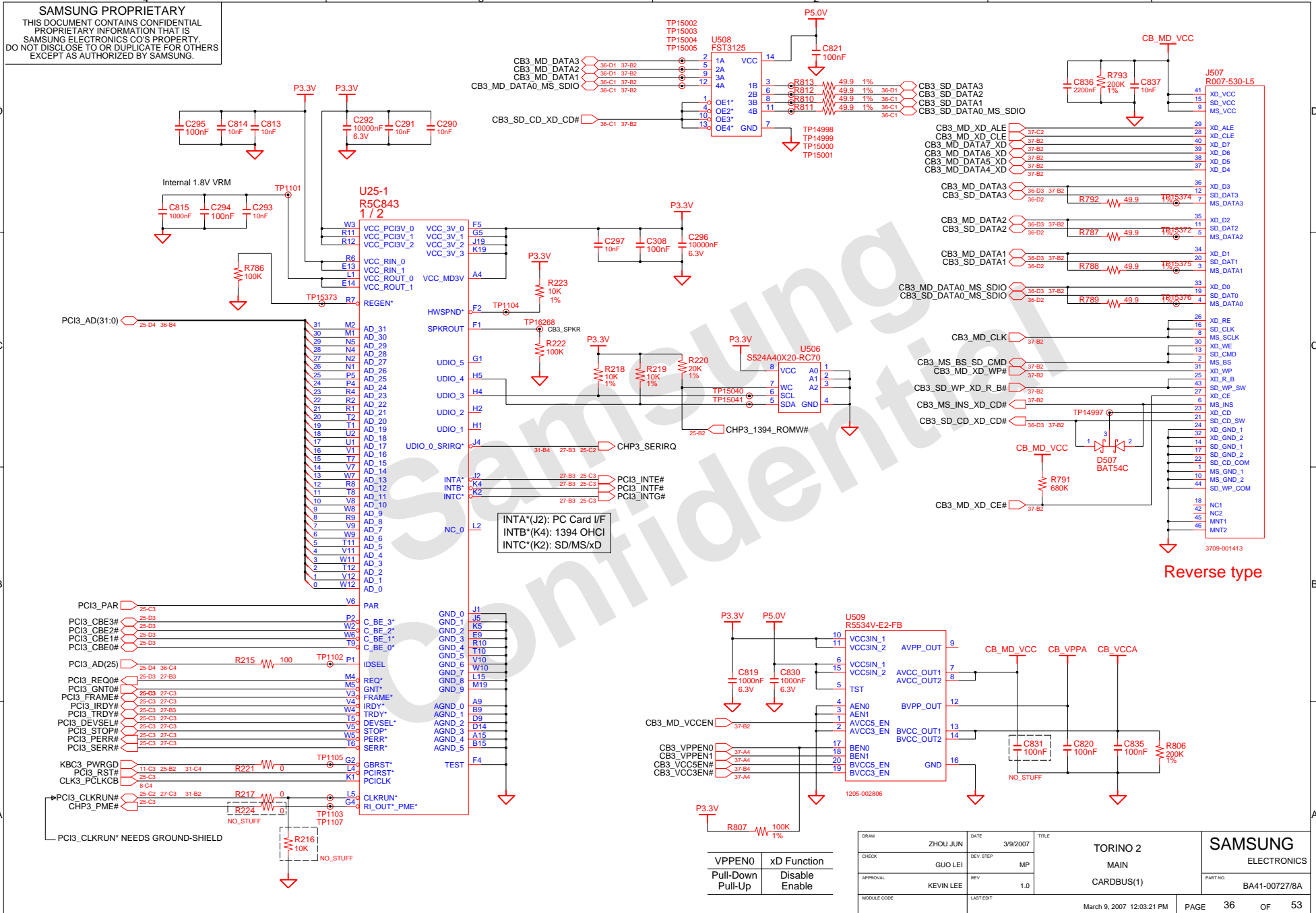


M1 HEAD
DIA
LENGTH
BA61-01090

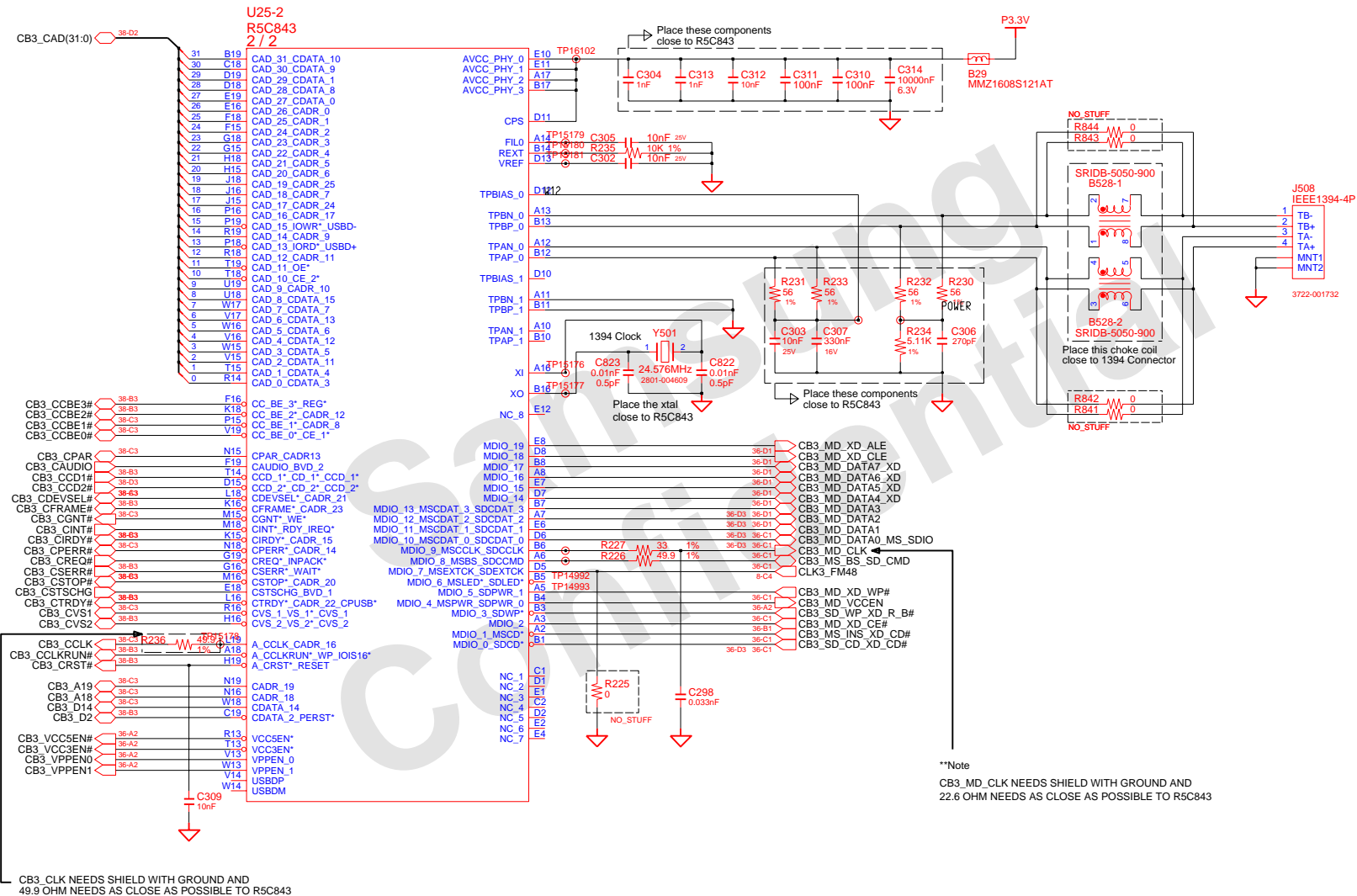
SAMSUNG
ELECTRONICS

PART NO. BA41-00727/8A

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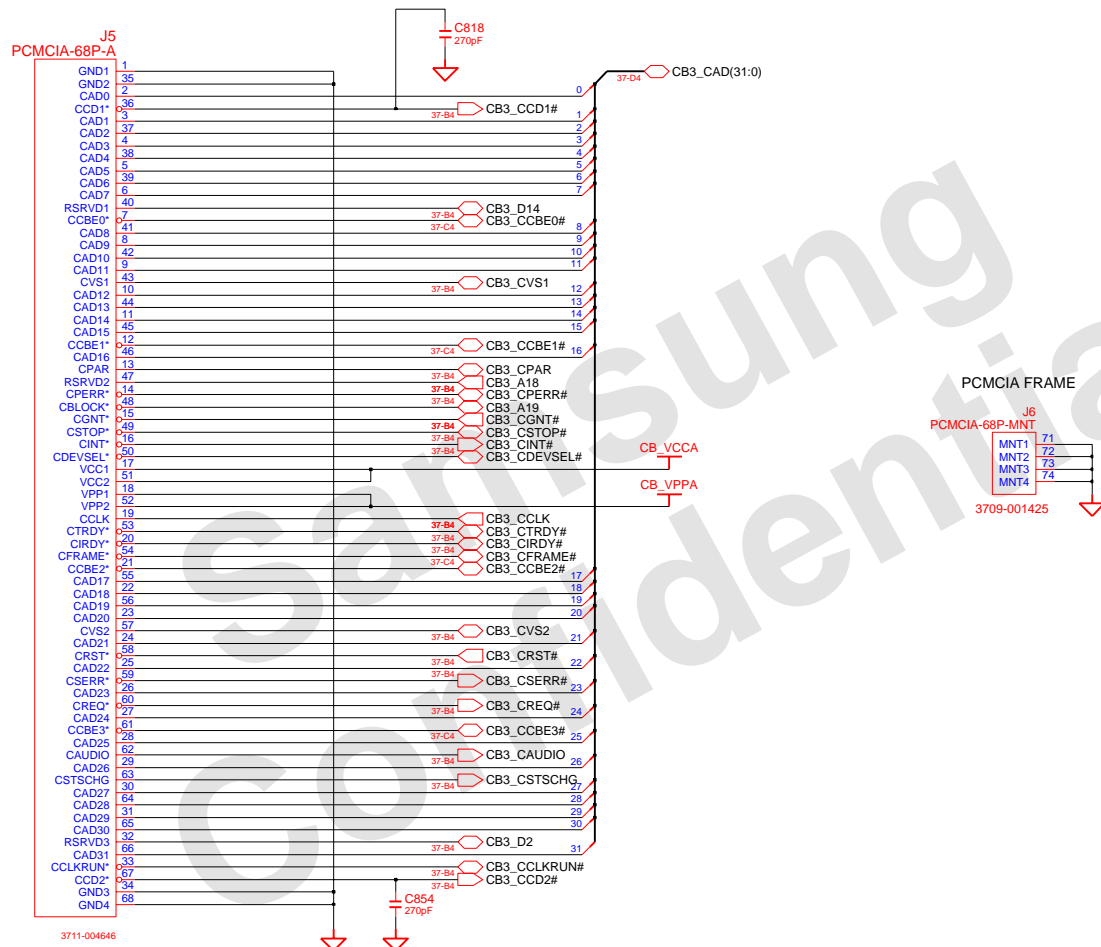
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**Note
CB3_MD_CLK NEEDS SHIELD WITH GROUND AND
22.6 OHM NEEDS AS CLOSE AS POSSIBLE TO R5C843

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV STEP	MP		MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		CARDBUS(2)	PART NO.
MODULE CODE		LAST EDIT				BA41-00727/8A
				March 9, 2007 12:03:21 PM	PAGE	37 OF 53

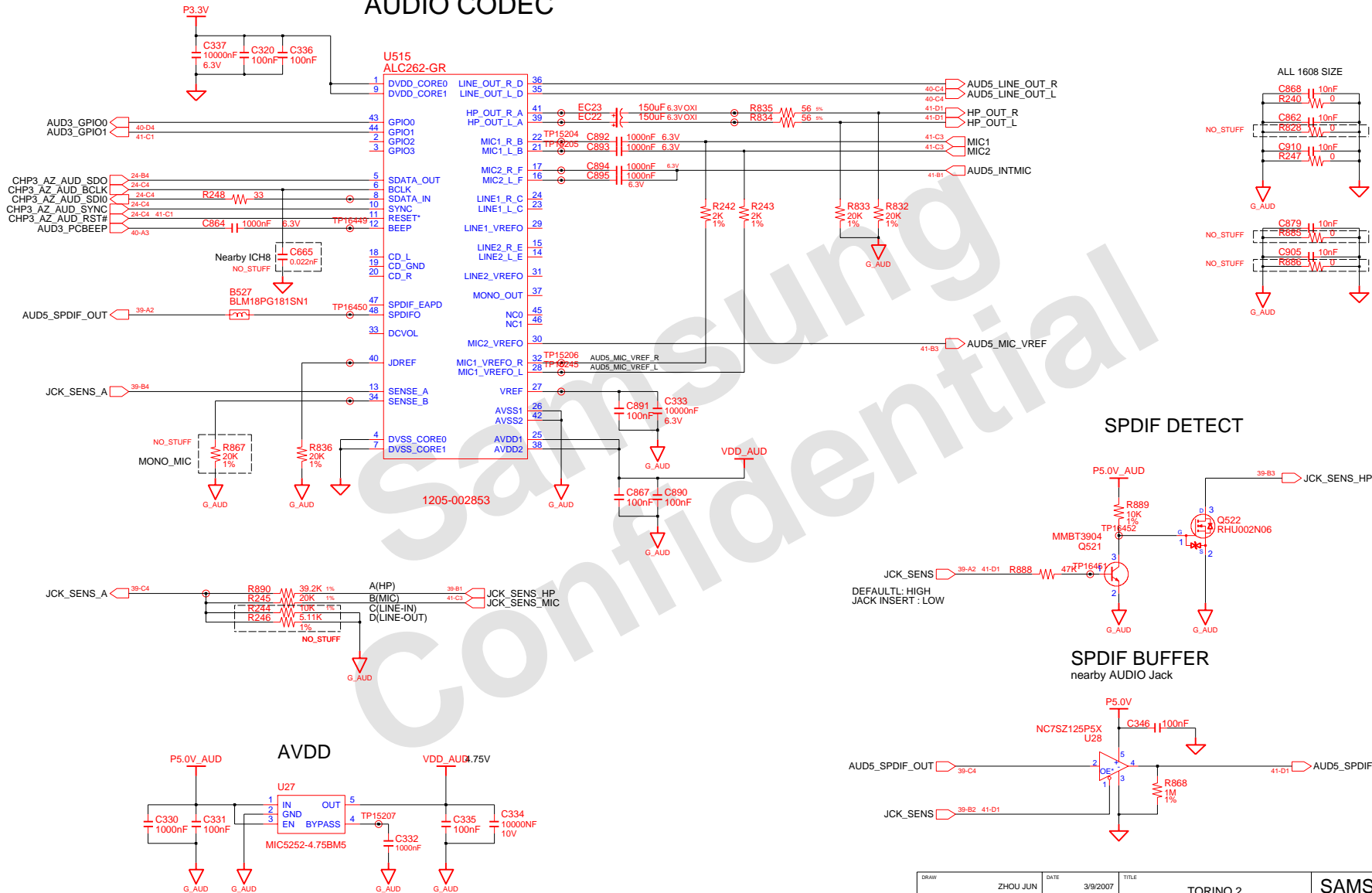
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DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	CARDBUS (3)	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0			PART NO.
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	38	OF 53

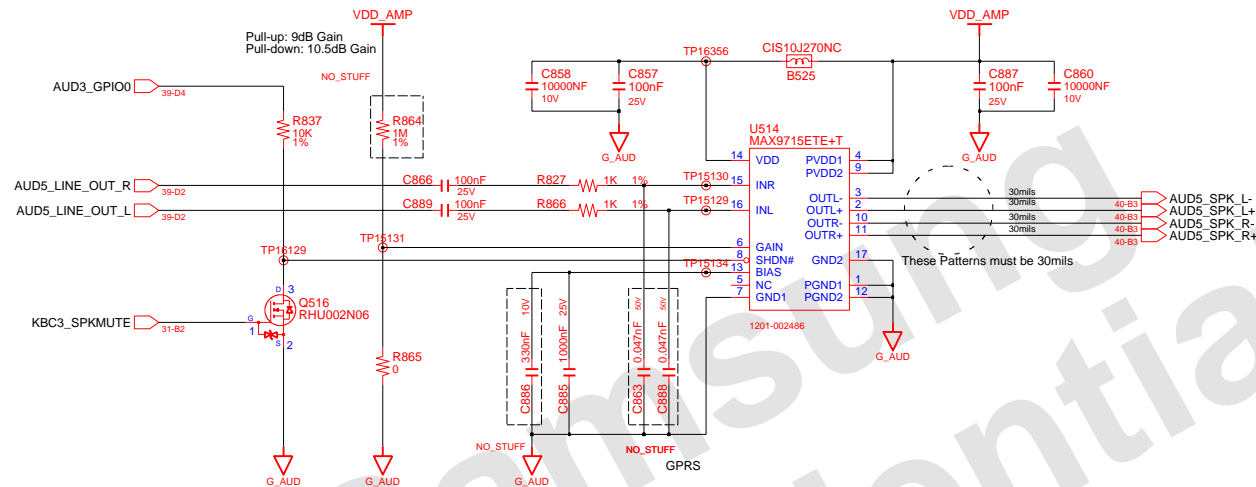
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AUDIO CODEC

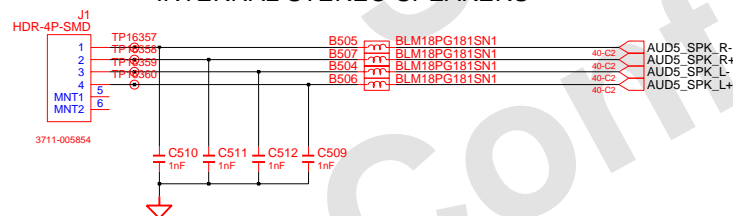


DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	AUDIO CODEC	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE	39 OF 53

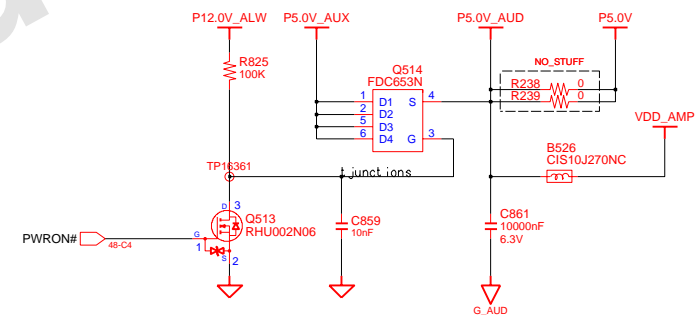
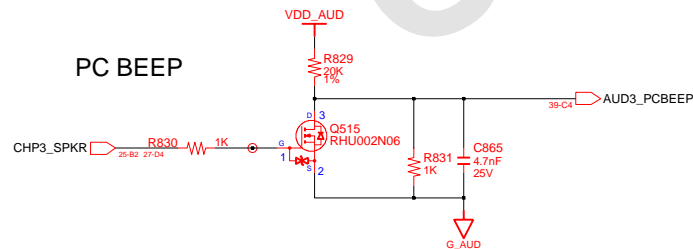
AUDIO AMP



INTERNAL STEREO SPEAKERS

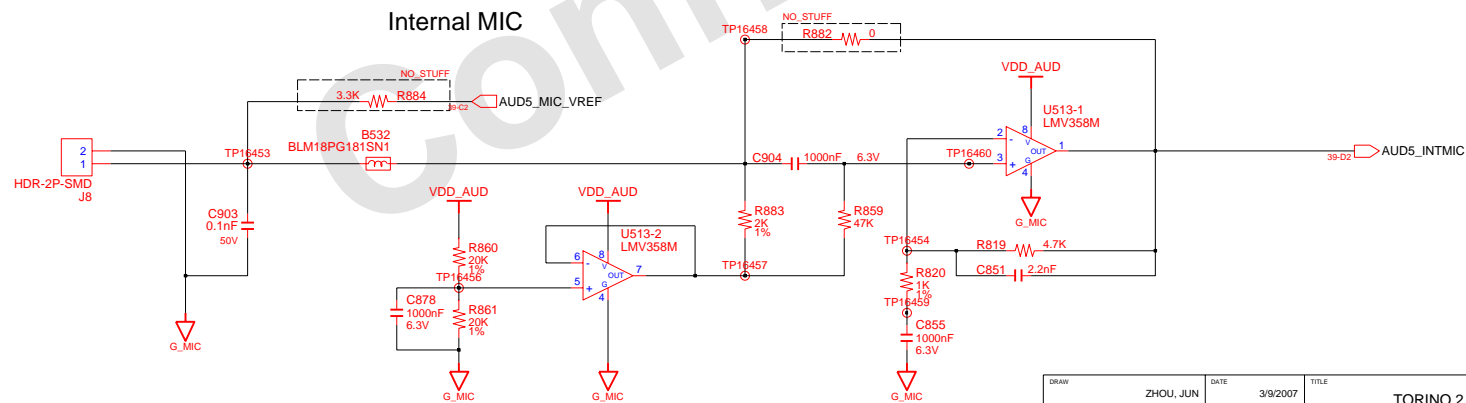
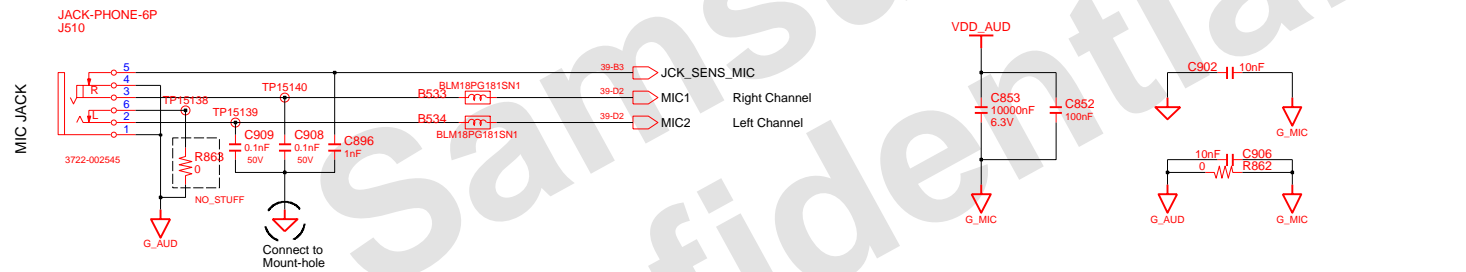
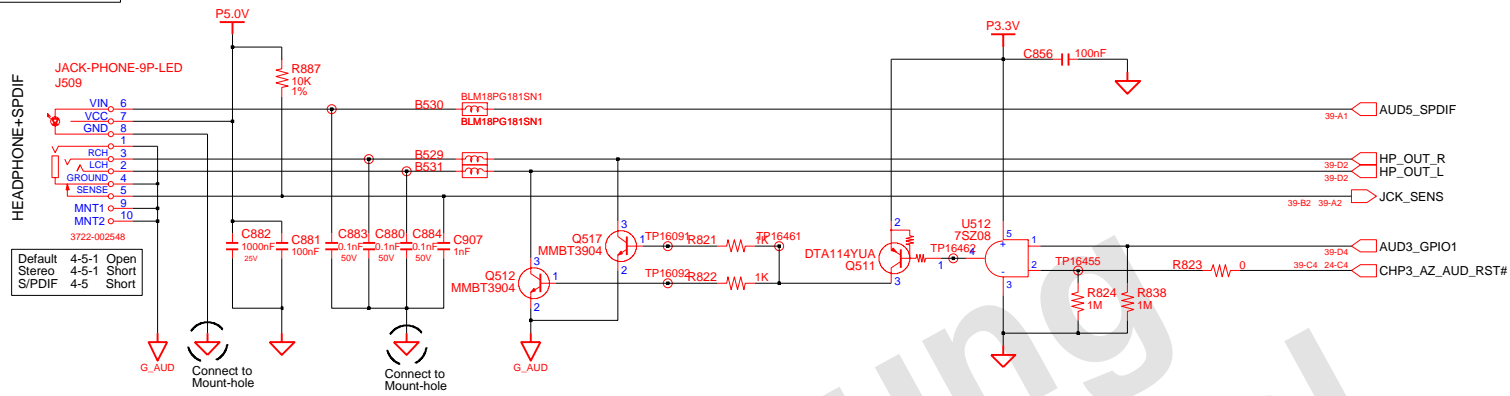


PC BEEP



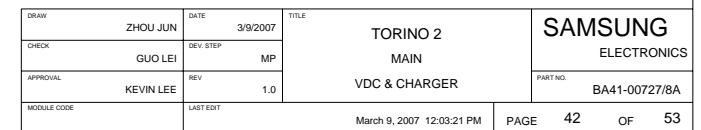
DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN AUDIO AMPLIFIER	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP			PART NO. BA41-00727/8A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE	40 OF 53

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DRAW	ZHOU, JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN AUDIO JACK	SAMSUNG ELECTRONICS
CHECK	GUO, LEI	DEV. STEP	MP			PART NO. BA41-00727/8A
APPROVAL	LEE, KEVIN	REV	1.0			
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE 41 OF 53	

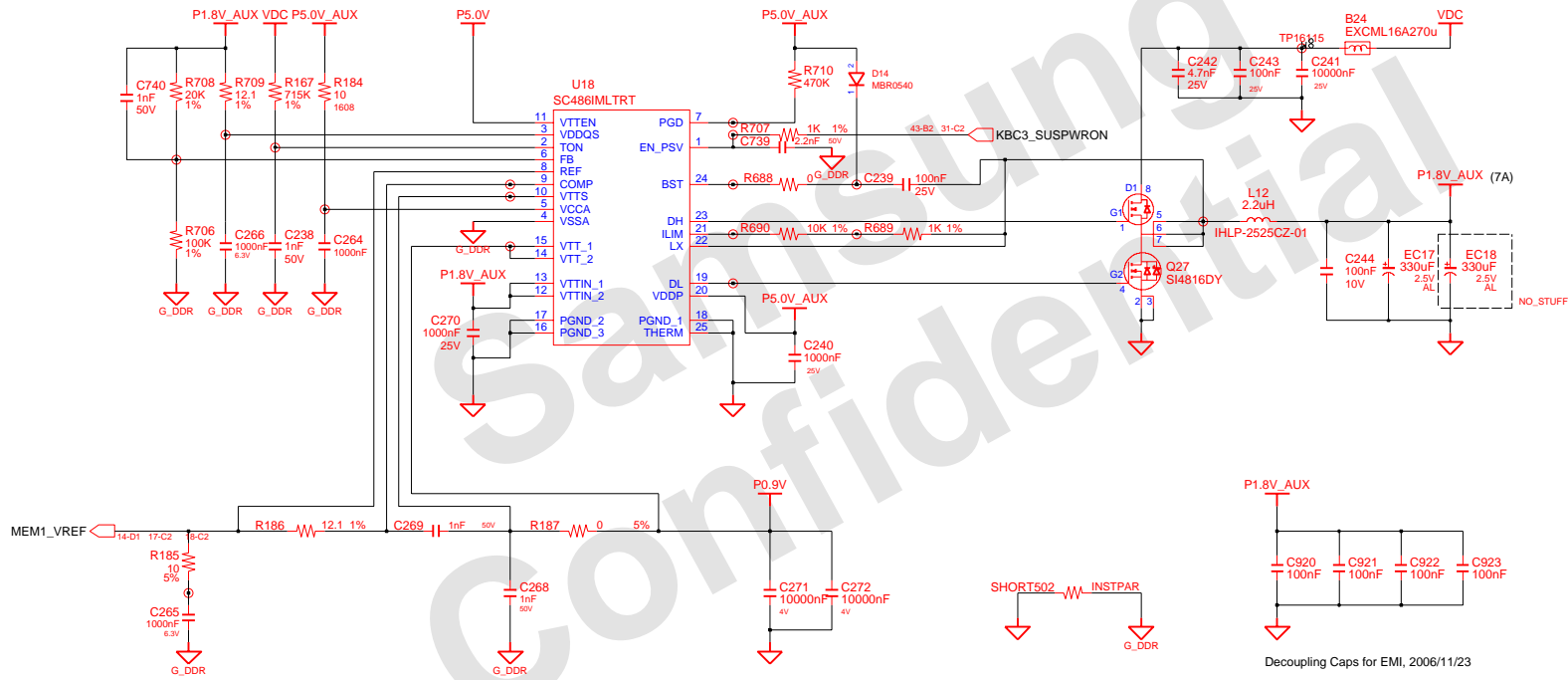
CHARGER & POWER MANAGEMENT



P5V_AUX & P3.3V_AUX

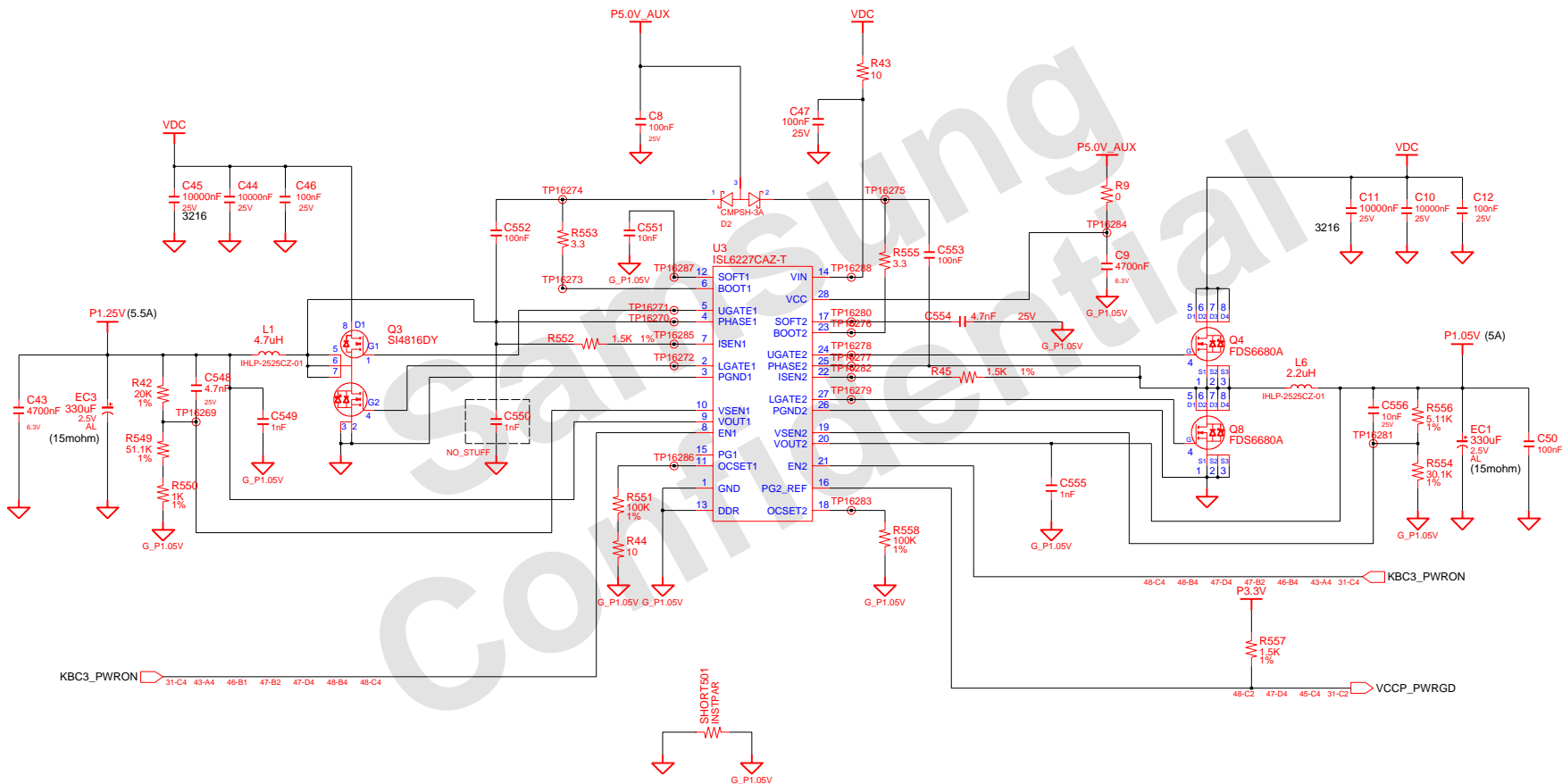


DDR2 POWER



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN DDR2 POWER	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP			PART NO. BA41-00727/8A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT				
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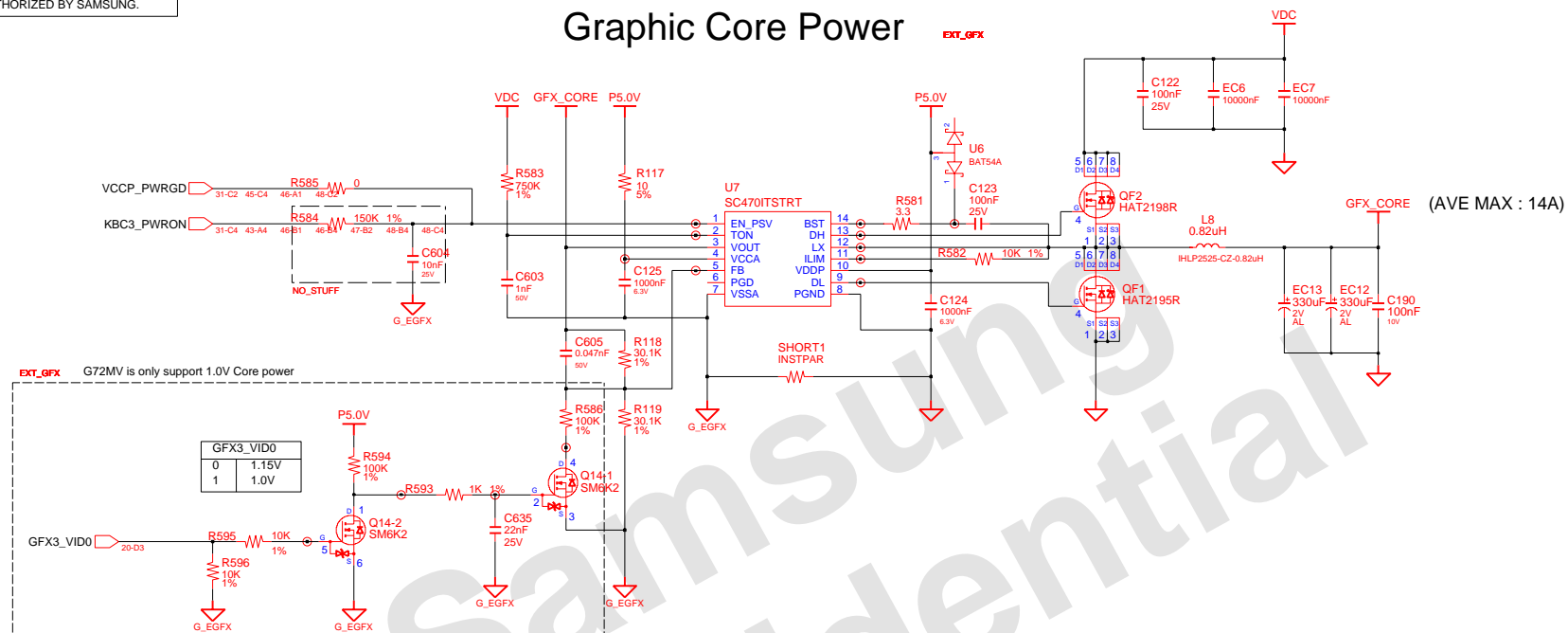
P1.25V & P1.05V



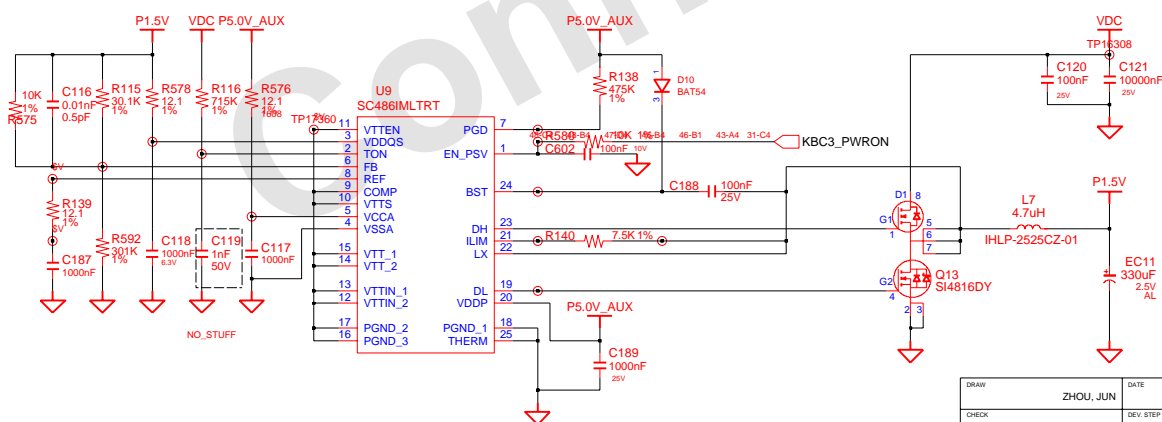
DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	P1.25V & P1.05V	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	46	OF 53

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Graphic Core Power

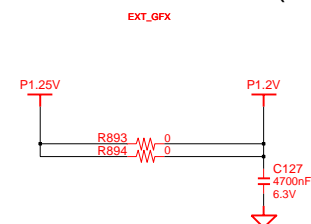


P1.5V

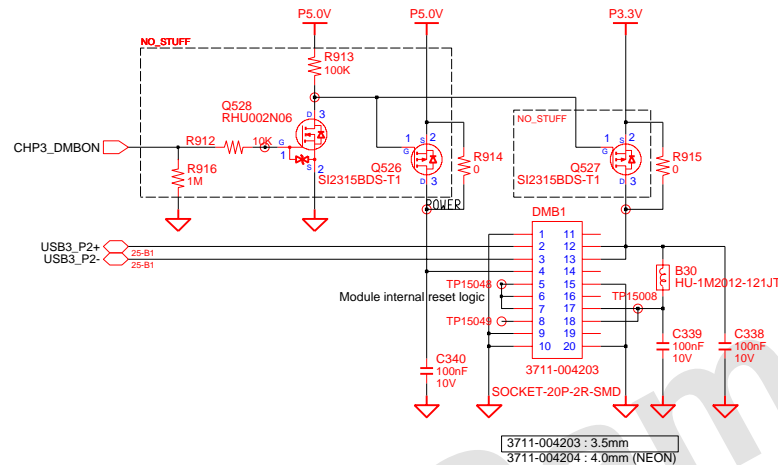


DRAW	ZHOU, JUN	DATE	3/9/2007	TITLE TORINO 2 MAIN GFX_CORE & P1.5V	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO, LEI	DEV STEP	MP		
APPROVAL	LEE, KEVIN	REV	1.0		
MODULE CODE	LAST EDIT				
March 9, 2007 12:03:21 PM				PAGE	47 OF 53

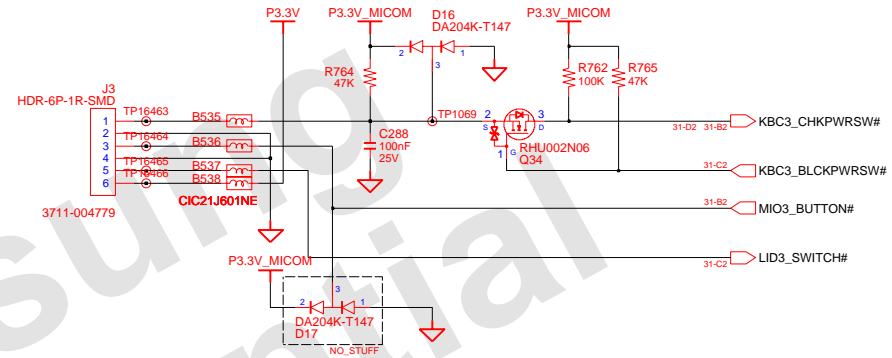
Switched Power On (P1.2V)

D:/users/mentor/Torino2/SR/T2 SR 0309

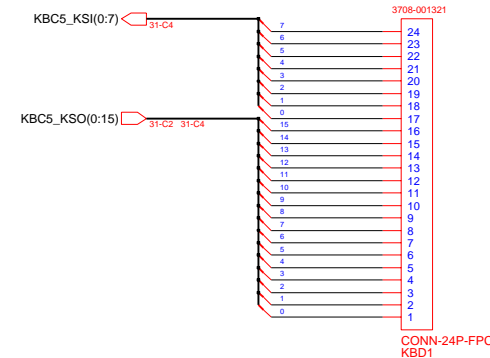
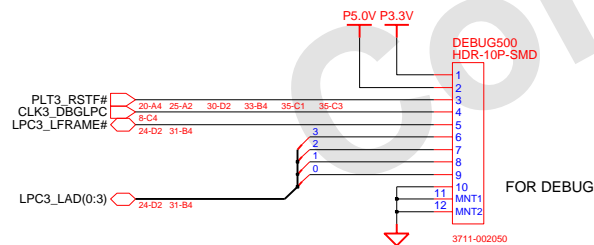
OPTION: DMB INTERFACE



Power Switch Connector



KEYBOARD



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	DMB & KBD & DEBUG & POWER S/W	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	49 OF 53

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LEDs circuit diagram showing three input signals: KBC3_NUMLED#, KBC3_CAPSLED#, and KBC3_SCLED#. Each signal is connected to a 7SZ08 buffer (U21, U23, U22 respectively). The outputs of these buffers are connected to LEDs: NUMLED#, CAPSLED#, and SCLED#.

LEDs

LEDs circuit diagram showing a P3.3V supply connected to five LEDs: LED1 (Green(Yellow) 1608size SML-310MT), LED2 (Green(Yellow) 1608size SML-310MT), LED3 (Green(Yellow) 1608size SML-310MT), LED4 (Green(Yellow) 1608size SML-310MT), and LED5 (Blue 1608size LTST-C193TBKT-AC). The LEDs are connected to signals: NUMLED#, CAPSLED#, SCLED#, HDD3_LED#, and WLED3_LED#.

LEDs circuit diagram showing a P3.3V_MICOM supply connected to two LEDs: LED6 (Amber 3.0*2.5) and LED7 (Blue 1608size LTST-C193TBKT-AC). The LEDs are connected to signals: KBC3_LED_CHARGE# and KBC3_LED_ACIN#.

LEDs circuit diagram showing a P3.3V_AUX supply connected to LED8 (Blue 1608size LTST-C193TBKT-AC). The LED is connected to signal: KBC3_LED_POWER#.

TOUCH PAD

TP_FFC PAD circuit diagram showing a P5.0V supply connected to TP15126 and TPAD1 connector. The connector is labeled TPAD1 CONN-12P-FPC 3708-002020.

TP_SWITCH circuit diagram showing a P5.0V supply connected to TP15124 and TP15125. The circuit includes two switches: SW2 and SW1, both labeled SW-TACT-4P Height : 5mm 3404-001311.

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS			
CHECK	GUO LEI	DEV. STEP	MP		MAIN				
APPROVAL	KEVIN LEE	REV	1.0		TOUCH PAD & LEDs				
MODULE CODE		LAST EDIT							
March 9, 2007 12:03:21 PM						PAGE	50	OF	53

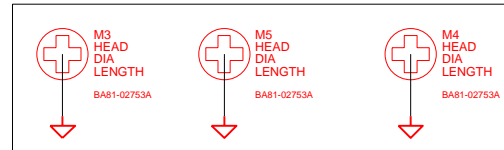
COM-22C-015(1996.6.5) REV. 3 4 3 2 1 D:\users\mentor\Torino2\SR\T2_SR_0309

D:/users/mentor/Torino2/SR/T2_SR_0309

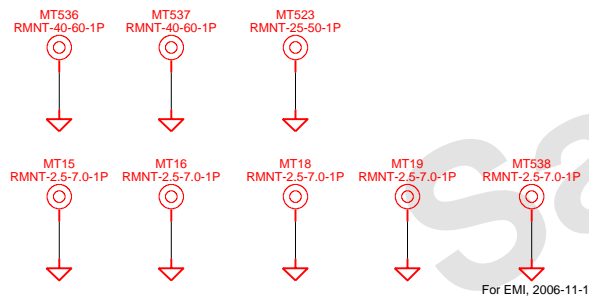
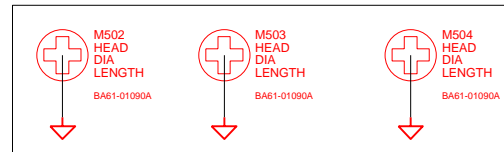
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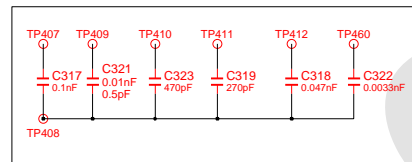
DMB-NUT



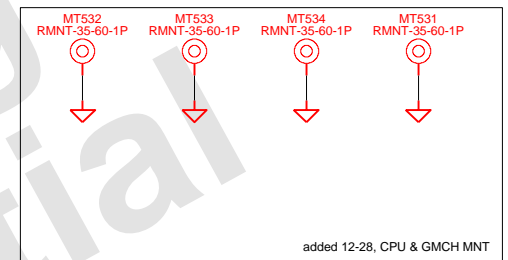
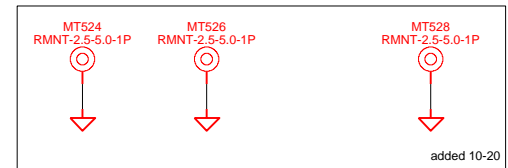
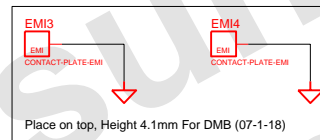
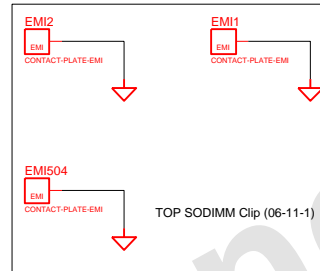
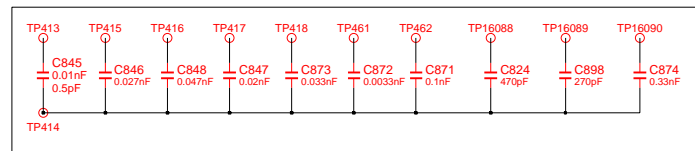
NUT for RHE, Locate on TOP of PCB



1 - 499 SIDE



500 - SIDE



REV1
1 O
2 O O3

PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/D)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		MOUNT HOLE	
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	51	OF 53

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TP17080○ADT3_SEL	TP17032○CB3_MD_DATA4_XD	TP16776○CTRL_18_25	TP16485○KBC5_KSO(11)	TP17143○PCB3_AD(24)	TP17228○G_MIC	TP17377○GROUND			
TP17081○AUD3_GPI00	TP17033○CB3_MD_DATA5_XD	TP16777○CTRL_12	TP16486○KBC5_KSO(12)	TP17144○PCB3_AD(25)	TP17231○G_P1.05V	TP17378○GROUND			
TP17082○AUD3_GPI01	TP17034○CB3_MD_DATA6_XD	TP16599○GMCH3_BMBUS#	TP16487○KBC5_KSO(13)	TP17145○PCB3_AD(26)	TP17233○G_P3.3V	TP17379○GROUND			
TP17083○AUD3_PCBEEP	TP17035○CB3_MD_DATA7_XD	TP16536○GMCH3_EXTTS0#	TP16488○KBC5_KSO(14)	TP17146○PCB3_AD(27)	TP17235○LDCD_VDD3V	TP17380○GROUND			
TP17084○AUD5_INTMIC	TP17036○CB3_MD_VCCEN	TP16537○GMCH3_EXTTS1#	TP16489○KBC5_KSO(15)	TP17147○PCB3_AD(28)	TP17236○LDO_P5.4V				
TP17085○AUD5_LINE_OUT_L	TP17037○CB3_MD_XD_ALE	TP16538○GMCH3_CHSYNCH	TP16490○KBC5_KSO(16)	TP17148○PCB3_AD(29)	TP17237○CP1.05V				
TP17086○AUD5_LINE_OUT_R	TP17038○CB3_MD_XD_CEF#	TP16543○IDE_A1	TP16491○KBC5_KSO(17)	TP17149○PCB3_AD(30)	TP17238○CP1.05V				
TP17087○AUD5_MIC_VREF	TP17039○CB3_MD_XD_CLE	TP16540○HP_OUT_L	TP16492○KBC5_KSO(18)	TP17150○PCB3_AD(31)	TP17239○CP1.05V				
TP17088○AUD5_SPDIF	TP17040○CB3_MD_XD_WP#	TP16541○HP_OUT_R	TP16493○KBC5_KSO(19)	TP17151○PCB3_AD(32)	TP17240○CP1.05V				
TP17089○AUD5_SPDIF_OUT	TP17041○CB3_MS_BS_SD_CMD	TP16542○IDE_A0	TP16494○KBC5_KSO(20)	TP17152○PCB3_AD(33)	TP17241○CP1.25V				
TP17090○AUD5_SPK_L+	TP17042○CB3_MS_INSG_XD_CD#	TP16543○IDE_A1	TP16495○KBC5_KSO(21)	TP17153○PCB3_AD(34)	TP17242○CP1.25V				
TP17091○AUD5_SPK_L-	TP17043○CB3_SD_CD_XD_CD#	TP16544○IDE_A2	TP16496○KBC5_KSO(22)	TP17154○PCB3_AD(35)	TP17243○CP1.25V				
TP17092○AUD5_SPK_R+	TP17044○CB3_SD_DATA0_MS_SDO	TP16545○IDE_CS1#	TP16497○KBC5_KSO(23)	TP17155○PCB3_AD(36)	TP17244○CP1.25V				
TP17093○AUD5_SPK_R-	TP17045○CB3_SD_DATA1	TP16546○IDE_CS3#	TP17353○KBC5_TCLK	TP17156○PCB3_AD(37)	TP17245○CP1.2V				
TP17094○BAT3_SMCCLK#	TP17046○CB3_SD_DATA2	TP16547○IDE_D(0)	TP17356○KBC5_TDATA	TP17157○PCB3_AD(38)	TP17246○CP1.2V				
	TP17047○CB3_SD_DATA3	TP16548○IDE_D(1)	TP16498○LAN3_ACTLED#	TP17158○PCB3_CBE0#	TP17247○CP1.2V				
TP17096○BAT3_TEMP#	TP16984○CB3_SD_WP_XD_R_B#	TP16549○IDE_D(10)	TP16499○LAN3_CLKREQ#	TP17159○PCB3_CBE1#	TP17248○CP1.2V				
TP17097○BAT3_VOLT#	TP16985○CB3_VCC3EN#	TP16550○IDE_D(11)	TP16500○LAN3_LINK1000#	TP17160○PCB3_CBE2#	TP17249○CP1.2V_LAN				
TP17098○CAPSLED#	TP16986○CB3_VCC5EN#	TP16551○IDE_D(12)	TP16501○LAN3_LINK10_100#	TP17161○PCB3_CBE3#	TP17250○CP1.2V_LAN				
TP17099○CB3_A18	TP16987○CB3_VPPEN#	TP16552○IDE_D(13)	TP17112○LAN3_VPDCLK	TP17162○PCB3_CLKRUN#	TP17251○CP1.2V_LAN				
TP17100○CB3_A19	TP16988○CB3_VPPEN	TP16553○IDE_D(14)	TP17113○LAN3_VPD0DATA	TP17163○PCB3_DEVSLE#	TP17252○CP1.2V_LAN				
TP17101○CB3_CAD(0)	TP16989○CHP3_1394_ROMW#	TP16554○IDE_D(15)	TP17114○LDCD3_BKLTCTRL	TP17164○PCB3_FRAME#	TP17253○CP1.5V				
TP17102○CB3_CAD(1)	TP16990○CHP3_AZ_AUD_BCLK	TP16555○IDE_D(16)		TP17165○PCB3_GNTO#	TP17254○CP1.5V				
TP17103○CB3_CAD(10)	TP16991○CHP3_AZ_AUD_RST#	TP16556○IDE_D(17)	TP17116○LDCD3_BKLTON	TP17166○PCB3_INT#A	TP17255○CP1.5V				
TP17104○CB3_CAD(11)	TP16992○CHP3_AZ_AUD_SDO	TP16557○IDE_D(18)		TP17167○PCB3_INT#B	TP17256○CP1.5V				
TP17105○CB3_CAD(12)	TP16993○CHP3_AZ_AUD_SDO	TP16558○IDE_D(19)	TP17118○LDCD3_EBID_DATA	TP17168○PCB3_INT#C	TP17257○CP1.8V				
TP17106○CB3_CAD(13)	TP16994○CHP3_AZ_AUD_SYNC	TP16559○IDE_D(20)		TP17169○PCB3_INT#D	TP17258○CP1.8V				
TP17107○CB3_CAD(14)	TP16995○CHP3_AZ_MDC_BCLK	TP16560○IDE_D(21)	TP17120○LPC3_LAD(0)	TP17170○PCB3_INT#E	TP17259○CP1.8V				
TP17108○CB3_CAD(15)	TP16996○CHP3_AZ_MDC_RST#	TP16561○IDE_D(22)	TP17121○LPC3_LAD(1)	TP17171○PCB3_INT#F	TP17260○CP1.8V				
TP17109○CB3_CAD(16)	TP16997○CHP3_AZ_MDC_SDI	TP16562○IDE_D(23)	TP17122○LPC3_LAD(2)	TP17172○PCB3_INT#G	TP17261○CP1.8V_AUX				
TP17110○CB3_CAD(17)	TP16998○CHP3_AZ_MDC_SDO	TP16563○IDE_DACK#	TP17123○LPC3_LAD(3)	TP17173○PCB3_INT#H	TP17262○CP1.8V_AUX				
TP17111○CB3_CAD(18)	TP16999○CHP3_AZ_MDC_SYNC	TP16564○IDE_DREQ	TP17124○LPC3_LFRAME#	TP17174○PCB3_IRDY#	TP17263○CP1.8V_AUX				
TP17048○CB3_CAD(19)	TP17000○								

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REVISION HISTORY

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NOTES

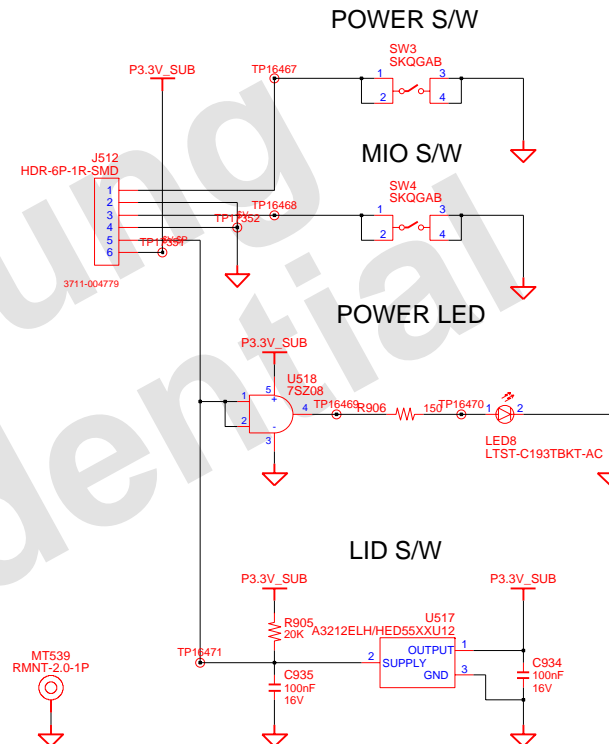
Note1. Schematic Revision: MP 1.0.0

Development Stage
PCB Version
Schematic Version

Note2. PCB SEC Code

Vendor: SGCE P/N: BA41-??????
Vendor: TPT P/N: BA41-??????

ON TOP BOARD



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	REVISION HISTORY	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	53	OF 53